## 5305 B



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## CERTIFICATION

Hewlett-Packard Company certifies that this instrument met its published specifications at the time of shipment from the factory. Hewlett-Packard Company further certifies that its calibration measurements are traceable to the United States National Bureau of Standards, to the extent allowed by the Bureau's calibration facility, and to the calibration facilities of other International Standards Organization members.

## WARRANTY AND ASSISTANCE

This Hewlett-Packard product is warranted against defects in materials and workmanship for a period of one year from the date of shipment. Hewlett-Packard will, at its option, repair or replace products which prove to be defective during the warranty period provided they are returned to Hewlett-Packard, and provided the preventive maintenance procedures in this manual are followed. Repairs necessitated by misuse of the product are not covered by this warranty. NO OTHER WARRANTIES ARE EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. HEWLETT-PACKARD IS NOT LIABLE FOR CONSEQUENTIAL DAMAGES.

Service contracts or customer assistance agreements are available for Hewlett-Packard products.

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# SECTION I XE 1300 MHz COUNTER <br> 5305 B 

## OPERATING AND SERVICE MANUAL

## SERIAL PREFIX: 1616A

This section applies directly to Model 5305B 1300 MHz Counters having Serial Prefix 1616A. This section is provided in loose-leaf form for incorporation into the 5300 Measurement System Manual. 5305A instruments are documented in a separate manual.

## NEWER INSTRUMENTS

This section with enclosed "Manual Changes" sheet applies directly to HP Model 5305B 1300 MHz Counters having Serial Prefix numbers above 1616A.

## OLDER INSTRUMENTS

Subsection VII of this document contains information pertinent to all older instruments.

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## SECTION I XE <br> 5305B 1300 MHz COUNTER

## SUBSECTION I

## GENERAL INFORMATION

## 9E-1-1. SCOPE OF MANUAL

9E-1-2. This manual provides operating and service information for the Hewlett-Packard Model 5305B 1300 MHz Counter. Information for the mainframes (5300A or 5300 B ) is contained in separate manuals. This manual is divided into eight sections containing the following information:

SECTION 1 GENERAL INFORMATION covers a description of the counter, equipment supplied, accessories, specifications, and recommended test equipment.

SECTION II INSTALLATION provides instructions for unpacking, inspection, preparation for use, shipment, and storage for the counter.

SECTION III OPERATION covers the counter's operating features including front-panel controls, input level considerations, and operating and selfcheck procedures.

SECTION IV THEORY OF OPERATION describes the counter's theory of operation.

SECTION V MAINTENANCE contains an incabinet performance check, adjustments, and troubleshooting information.

SECTION VI REPLACEABLE PARTS provides a complete list of the counter's replaceable parts and information for ordering parts.

SECTION VII MANUAL CHANGES provides information necessary to backdate the manual to cover earlier instruments.

SECTION VIII SCHEMATIC DIAGRAMS THEORY contains schematic diagrams, and component locators.

## 9E-1-3. DESCRIPTION

9E-1-4. The 5305B extends the frequency measuring capability of the 5300 Measuring System to the UHF range. The counter features burst or CW measurements to 1300 MHz , separate channels to cover $90 \mathrm{MHz}-1300 \mathrm{MHz}$ and 50 Hz to 100 MHz both with 20 mV rms sensitivity, high resolution mode for fast tone measurements, automatic gain control for both channels or manual attenuator control for the high frequency channel, fuse protected high frequency channel, and probe power plus accessory preamp for high sensitivity applications. When operating in the high resolution mode, a phaselocked multiplier gives 1000 times improvement in the resolution of audio tone measurements. This feature is especially useful for servicing equipment using tone modulation for digital encoding on the carrier.


The 5305B is applicable to mobile communication bands in addition to VHF and UHF TV transmissions plus TACAN/DME and ATC radar transponders.

9E-1-5. The 10855A Preamplifier is available to boost the UHF sensitivity of the counter by a minimum of 22 dB .

## 9E-1-6. INSTRUMENT IDENTIFICATION

9E-1-7. Hewlett-Packard instruments have a 2-section, 10 -character serial number ( 0000 A 00000 ) which is located on the rear panel. The 4 -digit serial prefix identifies instrument changes. If the serial prefix of your instrument differs from that listed on the title page of this manual, there are differences between this manual and your instrument. Instruments having higher serial prefixes are covered with a "Manual Changes" sheet included with this manual. If the change sheet is missing, contact the nearest Hewlett-Packard Sales and Service Office listed at the back of this manual. Instruments having a lower serial prefix than that listed on the title page, are covered in the backdating Section VII.

## 9E-1-8. EQUIPMENT SUPPLIED

9E-1-9. The 5305B is supplied with an operating and service manual.

## 9E-1-10. ACCESSORIES AVAILABLE

9E-1-11. For high-sensitivity UHF applications, the 10855A Preamplifier can be used with the 5305B. The 10855 A covers the 2 MHz to 1.3 GHz range with a gain of 22 dB minimum. Power requirements are +15 volts at $\approx 40$ mA . The 5305B has a front-panel connector to supply the required +15 volts to 10855 A .

## 9E-1-12. 5300A/5300B COMPATIBILITY

9E-1-13. The 5305B is fully compatible with either the 5300 A or the 5300B mainframe. Unlike the 5305A, a highstability time base is not available for the 5305B, however a high-stability time base is available for the 5300B.

## 9E-1-14. SPECIFICATIONS

9E-1-15. $\quad$ Specifications are listed in Table 9E-1-1.

## 9E-1-16. RECOMMENDED TEST EQUIPMENT

9E-1-17. Test equipment recommended for testing, calibration, and repair of the 5305B is listed in Table 1-2.

Table 9E-1-1. Specifications

## INPUT CHANNEL A (CW OR BURST)

Range: 90 MHz to 1300 MHz , prescaled by 16
Sensitivity: 20 mV rms
Impedance: $50 \Omega$
Attenuator: Continuously variable to give optimum noise suppression for signals up to 3.5 V rms.

Overload Protection: 5V rms, maximum. Input circuitry is fuse protected; fuse is located in BNC connector and is accessible from the front panel.

Operating Dynamic Range: $>47 \mathrm{~dB}$

## INPUT CHANNEL B (NORMAL AND HIGH RESOLUTION MODE)

Range: 50 Hz to 100 MHz , direct count in normal mode. 50 Hz to 10 kHz in high resolution mode. In the high resolution mode, the 5305B uses a phaselocked multiplier to increase resolution X1000 over normal measurement resolution.

Sensitivity: 20 mV rms
Impedance: $1 \mathrm{M} \Omega$ shunted by less than 40 pF .
Overload Protection: 250 V rms from 50 Hz to 10 kHz , declining to 10 V rms above 10 MHz .

Search Indicator: In high-resolution mode the "S" annunciator is lit whenever the input is beyond the proper frequency range, or too weak to measure, or during the brief acquisition time following signal interruption.

Automatic Hold: In high-resolution mode, the last valid reading is held in display when input is terminated.

## FREQUENCY MEASUREMENT

## RESOLUTION (SELECTABLE):

Normal Mode ( $\mathbf{5 0} \mathbf{~ H z}$ to $\mathbf{1 3 0 0} \mathbf{~ M H z ) : ~} 0.1 \mathrm{~Hz}$ to 10000 Hz in decade steps corresponding to gate times of 10 sec to 0.0001 sec in decade steps on channel B and to gate times of 160 s to .0016 s in decade steps on channel A.
High Resolution Mode ( $\mathbf{5 0} \mathbf{~ H z}$ to 10 kHz): 0.0001, $0.001,0.01,0.1,1,10 \mathrm{~Hz}$ corresponding to $10,1,0.1$, $0.01,0.001,0.0001$ second gate times on channel B.

Accuracy: $\pm 1$ digit displayed $\pm$ time base accuracy.
Display: $\mathrm{Hz}, \mathrm{kHz}, \mathrm{MHz}$ with positioned decimal point.

## GENERAL

Check: Counts internal 10 MHz reference frequency to check counting circuits.

Operating Temperature: $0^{\circ}$ to $50^{\circ} \mathrm{C}$.
Power Requirements: Nominally 12 watts including mainframe.

Weight: Net 1.0 kg (2-1/4 lbs.); Shipping 1.8 kg (4 lbs.)

Dimensions: With mainframe, 89 mm H (3-1/2")x 160 mm W (6-1/4") x 248 mm L (9-3/4").

Compatible Mainframes: 5300A (6 digits) or 5300B ( 8 digits). 5300 B is recommended.

Accessories: 10855A Preamp (22 dB gain).

Model 5305B General
Information
Table 9E-1-2. Recommended Test Equipment

| Instrument | Required Characteristics | Recommended Type |
| :--- | :--- | :--- |
| Oscilloscope | 50 MHz Bandwidth | HP 180A |
| Vertical Plug-In | $50 \mathrm{mV} / \mathrm{div}$ Sensitivity | HP 1801A |
| Time Base Plug-In | 50 MHz Bandwidth | HP 1821A |
| Sampling Plug-In | 1000 MHz | HP 1810A |
| Optical Sampling Plug-In | If desired to measure up to 1300 MHz | HP 1811A/1432A |
| Synthesized Signal Generator | 1300 MHz | HP 8660B/86602A |
| Power Meter | $-30 \mathrm{dBm} \mathrm{to}+10 \mathrm{dBm}$ | HP 435A |
| Power Sensor | 90 MHz to 1300 MHz | HP 8481A |
| Test Oscillator | 50 Hz to $10 \mathrm{MHz} \mathrm{3V} \mathrm{rms}$ | HP 651B |
| Mainframe |  | HP 5300B |
| Digital Voltmeter | $-5 \mathrm{~V} \mathrm{to}+20 \mathrm{~V} \mathrm{dc}$ | HP 5306A |
| Power Splitter | 50 ohms 90 MHz to 1300 MHz | HP 11667A |
| Scope Probe | $10: 11 \mathrm{Meg} \Omega$ | HP 10004D |

## SECTION IX E

## 5305B 1300 MHz COUNTER SUBSECTION II INSTALLATION

## 9E-2-1. UNPACKING AND INSPECTION

9E-2-2. If the shipping carton is damaged, ask that the carrier's agent be present when the instrument is unpacked. Inspect the instruments for damage, such as scratches, dents, broken knobs, etc. If the instrument is damaged or fails to meet performance tests when used with the 5300B Measuring System, notify the carrier and the nearest Hewlett-Packard Sales and Service Office immediately. Performance check procedures are located in Section IX E5, and Sales and Service Offices are listed in Section VI of the 5300 B portion of the manual. Retain the shipping carton and the padding material for the carrier's inspection. The Sales and Service Office will arrange for the repair or replacement of the instrument without waiting for the claim against the carrier to be settled.

## 9E-2-3. STORAGE AND SHIPMENT

9E-2-4. PACKAGING. To protect valuable electronic equipment during storage and shipment, always use the best packaging methods available. Your HewlettPackard Sales and Service Office can provide packaging material, such as that used for original factory packaging. Contract packaging companies in many cities can provide dependable custom packaging on short notice. Here is one recommended packaging method:
a. The original container is a corrugated cardboard box with 200 lbs. burst test (HP Part No. 9211-1620). The instrument is secured and protected, while in the box, by a
top and bottom molded frame of polystyrene (HP Part No. $9220-1545$ ). Also included with the instrument is a plastic dust-protection cover (HP Part No. 9220-1762).

9E-2-5. ENVIRONMENT. Conditions during storage and shipment should be normally limited as follows:
a. Maximum altitude: $25,000 \mathrm{ft}$.
b. Minimum temperature: $-40^{\circ} \mathrm{F}\left(-40^{\circ} \mathrm{C}\right)$.
c. Maximum temperature: $+167^{\circ} \mathrm{F}\left(+75^{\circ} \mathrm{C}\right)$.

## 9E-2-6. INSTALLATION AND REMOVAL OF PLUG-ON

9E-2-7. The 5305B 1300 MHz Counter must be used with a mating 5300A or B Measuring System, before any measurements can be made. To mate the 5305B 1300 MHz Counter with a 5300 Measuring System, see Figure 2-1 and Paragraph 2-11 in the 5300 portion of the manual.

## 9E-2-8 PORTABLE OPERATION.

9E-2-9. The use of the HP Model 5310A Battery Pack enables the 5300 Measuring System and 5305B 1300 MHz Counter to be used in areas removed from ac power sources. The 5310A Battery Pack typically provides 5 hours of portable operating time before recharging. Tables $1-2$ and 1-4 in 5300 portion of the manual lists the HP 5310A Battery Pack as an available accessory. Documentation on the 5310A is also included in Section IV through VIII of the 5300 portion of the manual. To prepare the 5300/5305B for portable operation, refer to Paragraph 2-13 and Figure 2-2 in the 5300 portion of the manual.

# SECTION IX E <br> 5305B 1300 MHz COUNTER 

## SUBSECTION III

## OPERATION

## 9E-3-1. INTRODUCTION

9E-3-2. This section covers operating information for the 5305B including a description of controls, indicators, and connectors, resolution, input levels, and operating procedures for frequency measurements and self check.

## 9E-3-3. OPERATING CHARACTERISTICS

9E-3-4. The 5305B Counter performs frequency measurements by means of two separate input channels. These channels provide a combination of low frequency measurements and high-sensitivity, high frequency measurements. Measurement capability is applicable to all frequencies in the VHF and mobile communication bands in addition to a significant portion of the UHF band. The 10855A Preamplifier can be used to boost the UHF input sensitivity by 22 dB .

9E-3-8. The frequency range of Channel $B$ depends on the mode of operation - normal or high resolution. In the normal mode, Channel B covers 50 Hz to 100 MHz . With high-resolution selected, Channel B covers 50 Hz to 10 kHz . The high resolution mode uses a phase-locked multiplier to increase resolution by a factor of 1000. Input impedance is 1 Megohm shunted by less than 40 pF . A 10 to 1 divider probe can be used to increase the input impedance to 10 Megohms.

## 9E-3-9. RESOLUTION

9E-3-10. The 5305B has a RESOLUTION switch which determines the least-significant digit (LSD) displayed. For example, with an input of $123,456 \mathrm{~Hz}$, setting the RESOLUTION switch to 1 kHz places the " 3 " in the LSD. Setting the switch to 10 Hz , places the " 5 " in the LSD. Resolution can be expressed in terms of the counter's gate time, as shown in Table 9E-3-1.

## 9E-3-5. Input channels

9E-3-6. Two input channels are provided, Channel A - 90 MHz to 1300 MHz and Channel B-50 Hz to 100 MHz . Both channels have 20 mV rms sensitivity.

9E-3-7. Channel A prescales the input frequency by 16 and can be used to measure CW or burst signals. Input coupling is ac. An AGC circuit is included to give a dynamic range of $>47 \mathrm{~dB}$. An internal fuse, located inside the input jack, protects circuitry from overloads greater than 5 V rms. Note that a blown fuse may not prevent the counter from measuring high-frequency inputs. In this instance, the counter's circuitry is no longer protected,ie.,as it would be at lower frequencies by a good open circuit. If the fuse blows, a replacement fuse is supplied with the instrument.

Table 9E-3-1. Resolution vs Gate time

| RESOLUTION | GATE TIME |  |
| :---: | :---: | :---: |
|  | $\mathbf{8 0} \mathbf{~ M H z}$ | $\mathbf{1 1 0 0} \mathbf{~ M H z}$ |
| .1 Hz | 10 s | 160 s |
| 1 Hz | 1 s | 16 s |
| 10 Hz | .1 s | 1.6 s |
| 100 Hz | .01 s | .16 s |
| 1 kHz | 1 ms | 16 ms |
| 10 kHz | .1 ms | 1.6 ms |

Figure 9E-3-1. dBm-to-Voltage Conversions


Model 5305B
Operation

## 9E-3-11. 1300 MHz Channel Input Levels

$9 \mathrm{E}-3-12$. The 1300 MHz channel is a $50-\mathrm{ohm}$ system with a maximum input of 5 V rms. Figure 9E-3-1 provides a conversion scale for determining respective levels of
voltage, power, and dBm. This scale applies to a 50 -ohm system and is not applicable to the 100 MHz channel. The shaded area represents the specified operating range of the 1300 MHz channel.

Figure 9E-3-2. Front Panel Controls, Connectors, and Indicators


## NOTE

The "S" annunciator on the 5300 lights during the high-resolution mode whenever the input is beyond the proper frequency range, or too weak to measure, or during acquisition following signal interruption.

1. $90 \mathrm{MHz}-1300 \mathrm{MHz}$ Input Jack. With RANGE switch set to A, accepts input frequencies from 90 MHz to 1300 MHz . Input sensitivity is 20 mV .50 ohm input impedance and ac coupled. Maximum input is 5 V rms. Fuse is located inside jack. Use a BNC connector as a wrench to remove and tighten the fuse jack. Replacement HP part number for fuse is 2110-0301.
2. ATTENUATOR Control. Provides manual control of input attenuator circuit for 1300 MHz channel. MIN position provides minimum signal attenuation. MAX position provides maximum attenuation. AGC circuit gives $>47 \mathrm{~dB}$ dynamic range.
3. PREAMP POWER. Supplies $+15 \mathrm{~V} @ \approx 40 \mathrm{~mA}$ to power 10855A Preamplifier.
4. 100 MHz Input Jack. When RANGE is set to B,
accepts input frequencies from 50 Hz to 100 MHz . With RANGE set to B HIGH RESOLUTION, accepts frequencies up to 10 kHz . Input sensitivity is 20 mV rms. Input impedance is 1 Megohm shunted by less than 40 pF . Channel is ac coupled.
5. RANGE switch. Allows selection of either of the two input channels or the self-check mode. In the CHK position, allows system to count the internal 10 MHz clock signal. Measurement is not affected by signals connected to the input jacks.
6. RESOLUTION Switch. The counter's leastsignificant digit displays the measured resolution of the input signal that is selected with the switch. For example, 10 Hz selected with $6,789 \mathrm{~Hz}$ input frequency: counter displays 6.78 kHz .

Figure 9E-3-3. Self-Check Measurements


1. Apply input power to 5300 ac receptacle.
2. Turn counter on with 5300 SAMPLE RATE control Adjust SAMPLE RATE for minimum display time (full ccw).
3. Set RANGE switch to CHK position. Display is a function of RESOLUTION switch.

| Resolution <br> Selector | Display | Annunciator | Display | Annunciator |
| :---: | :---: | :---: | :---: | :---: |
|  | 5300A Self Check |  | 5300B Self Check |  |
| 10 kHz | 0010.00 | $\mathrm{M}, \mathrm{Hz}$ | $000010.00 \pm 1$ | $\mathrm{M}, \mathrm{Hz}$ |
| 1 kHz | 010.000 | $\mathrm{M}, \mathrm{Hz}$ | $00010.000 \pm 1$ | $\mathrm{M}, \mathrm{Hz}$ |
| 100 Hz | 10.0000 | $\mathrm{M}, \mathrm{Hz}, \mathrm{C}$ | $0010.0000 \pm 1$ | $\mathrm{M}, \mathrm{Hz}, \mathrm{C}$ |
| 10 Hz | 0.00000 | $\mathrm{M}, \mathrm{Hz}, \mathrm{C}$ | $010.00000 \pm 1$ | $\mathrm{M}, \mathrm{Hz}, \mathrm{C}$ |
| 1 Hz | $\bullet 000.000$ | $\mathrm{~K}, \mathrm{~Hz}, \mathrm{C}$ | $10000.000 \pm 1$ | $\mathrm{~K}, \mathrm{~Hz}, \mathrm{C}$ |
| .1 Hz | $\bullet 00.0000$ | $\mathrm{~K}, \mathrm{~Hz}, \mathrm{C}$ | $0000.0000 \pm 1$ | $\mathrm{~K}, \mathrm{~Hz}, \mathrm{C}$ |

- Indicates overflow light is on.

Figure 9E-3-4. 100 MHz Channel Frequency Measurements


1. Apply power to 5300 ac receptacle.
2. Turn counter on with 5300 SAMPLE RATE control.
3. Set RANGE switch to B position.
4. Connect input signal to 100 MHz jack.
5. Set RESOLUTION switch for number of digits desired in display.
6. Adjust SAMPLE RATE control for convenient interval between measurements.
7. For high resolution, set RANGE switch to B 10 kHz MAX - HIGH RESOLUTION X1000. This limits the input frequency to 10 kHz but gives resolution up to 4 decimal places.

8. Apply input power to ac receptacle.
9. Turn counter on with 5300 SAMPLE RATE control.
10. Set RANGE switch to A position.
11. Set RESOLUTION switch to 10 K .
12. Set ATTENUATOR control to MIN position.

CAUTION
Input level must not exceed 5V rms.
6. Connect input signal to $90 \mathrm{MHz}-1300 \mathrm{MHz}$ jack.
7. Adjust ATTENUATOR control until counter stops displaying, then back again until counter gives a stable display of the proper frequency.

# SECTION IX E 5305B 1300 MHz COUNTER 

## SUBSECTION IV THEORY OF OPERATION

## 9E-4-1 INTRODUCTION.

9E-4-2. The 5305B consists of two assemblies, Logic Board A1 and 1300 MHz amplifier A2. Two input channels for frequency measurements are provided, Channel A accepts signals from 90 MHz to 1300 MHz and Channel B is used for signal inputs up to 100 MHz . Channel B signals are connected directly to the A1 Logic Board. Channel A signal inputs are routed to the 1300 MHz Amplifier A2. The following theory describes the two assemblies.

## 9E-4-3. A1 LOGIC BOARD

9E-4-4. A1 consists of the following major circuits; High-Impedance Amplifier, Counting, Frequency Multiplier, and 1300 MHz . Paragraphs 9E-4-5 through 9E-4-30 describe the theory of operation for these circuits.

## 9E-4-3 High Impedance Amplifier

9E-4-6. The high-impedance amplifier consists of three main stages: (1) An input buffer (Q1, Q2) that provides high input impedance at unity gain. (2) An AGC (automatic gain control) amplifier (U6B and C) to generate the required gain to maintain a relatively constant output with a wide range of inputs. This assures that the counter will read the proper frequency in the presence of noisy input signals. (3) A Schmitttrigger U6A and driver U16 to convert noisy, slowchanging signals to "clean" square waves with fast rise times to drive the counting circuits.

9E-4-7. INPUT CIRCUIT OPERATION. The signal enters the amplifier through dc blocking capacitor C 4 , which, along with C7, R10, R7, CR4, and CR2, protects against large ac peaks at low frequencies. At high frequencies, R7 and CR4 and CR2 provide the input protection for the unity-gain boot-strapped sourcefollower circuit Q1 and Q2.

9E-4-8. AGC OPERATION. For small signal levels (below $\approx 10 \mathrm{mV} \mathrm{rms}$ ), the output of U 8 pin 1 is $<.6 \mathrm{~V}$ (with no input signal, the voltage is about -3.5 V ). This causes Q5 and Q4 to turn off which results in Q3 turning on.

The low source-to-drain impedance ( $50 \Omega$ ) shunts resistor R12. The signal passes unattenuated through Q3 to amplifier U6C. The output at $\mathrm{U} 6 \mathrm{~B}(6)$ feeds the signal through R33 and C25. The signal is converted to de by CR9, C24 and R35. As the input signal level increases, the AGC voltage at U8(1) increases above .6 volts to a maximum of $\approx+2$ volts. Both CR5 and CR7 begin to conduct and their resistance drops rapidly to a few ohms, shunting most of the signal to ground. At the same time, Q5 begins to conduct and causes Q4 to conduct.

9E-4-9. When Q4 is full on, it generates more than -5 volts gate-to-source voltage on Q3. This tends to "pinch off" Q3, thereby raising its resistance. With R6 as the main signal path, only a small portion ( 10 to $20 \mathrm{mV} \mathrm{p-p}$ ) of a large input signal ( 10 V p-p) drives amplifier U6C. This prevents saturation of U6, and prevents the degradation in signal-tonoise ratio that saturation would cause. Thus Schmitt trigger U6A is triggered only by the largest signal at the input and not noise.

9E-4-10. DC FEEDBACK AND DUTY CYCLE CONTROL. The first two amplifier stages of U6 have a fixed voltage gain of typically 30 to 40 with a bandwidth of 100 MHz . U8 monitors the output of U6B and regulates the dc level at about 1.3 V , regardless of temperature and device differences.

9E-4-11. SCHMITT TRIGGER AND OUTPUT. The third stage of U6 is used as a Schmitt trigger.This circuit shapes the .6 V p-p sine wave at its input into a .8 V p-p square wave. It will respond to signals less than 0.3 V p-p in amplitude and thus rejects noise on the input signal. U16 amplifies the square wave and converts it to TTL levels to drive U22.

## 9E-4-12. Counting Circuits

$9 \mathrm{E}-4-13$. The following paragraphs describe the counting circuits that are common to all input modes. The RANGE switch enables one of four NOR gates to pass its respective signal to the Main Gate U13A and to the arm flip-flop, U17B. The signals are (1) Channel A divided by 16, U12C; (2) Channel B, U12A; (3) Channel B times 1000(PLO), U12B; and (4) 10 MHz clock (check), U13B.

9E-4-14. In the Channel B mode, for example, the operation is as follows. Once the sample rate runs down and the INHIBIT line returns high, the next signal pulse from U12A sets U17B. The High output from U16D(13) arms the counter by allowing the CLOSE line to go High. The arm signal also passes the 10 MHz clock signal to the mainframe through U10A and U10B. The mainframe responds by clocking U17A with a LOG 0 pulse, which sets the Q output Low and opens the Main Gate. The signal now passes to the decade counter, U14, where it is divided by 10 . The data output of U14 feeds U13C, which provides a $60 / 40$ duty cycle of the divided signal to level translators Q9 and Q10. The signal then enters the main frame's counting assembly on the F1 line.

## NOTE

U6, 12. 13, 14, 17, and 25 are ECL devices that are connected to the +5 V supply. They set the logic states to $\approx 4.3 \mathrm{~V}$ for a High and $\approx$ 3.2 V for a Low.

9E-4-15. The measurement ends when the count in the mainframe's Time Base decade reaches its capacity. The decade then outputs a TB OUT pulse. The LOG pulse immediately following sets the MGFF and disables the Main Gate, U13A. During the measurement, the three-state data latch, U5, does not accept any new input data. Its output, however, is enabled periodically by the Low pulse from $\mathrm{U} 4(15)$. Because of the counting decade in the 5305B, U4 alters the Digital Address codes so the digits are correctly placed in the display. Therefore, the $\Sigma 4$ output goes Low for one count out of eight and switches the latch from its high impedance state to the low impedance (active) state.

This occurs for each scan of the display. When the measurement ends, the XFER line enables U4 and new data enters the latch with the next clock pulse from U11(4).

## 9E-4-16. Frequency Multiplier

9E-4-17. The Frequency Multiplier circuit contains a PLO (Phase-Locked Oscillator) that is used when the RANGE switch is set to the B 10 kHz MAX position. The PLO multiplies the Channel B input frequency by 1000 . which means the display's count will be 1000 times higher than normal. Thus, for a 51.234 Hz input and a 1 sec gate time, the counter's display would be 00.051 kHz without the PLO and 51.234 Hz with the PLO. The readout is corrected by changing the annunciator from MHz to kHz or kHz to Hz . The phase detector block diagram is shown in Figure 9E-4-1.

9E-4-18. PHASE DETECTOR. The signal to be measured passes through the channel B amplifier and is applied to pin 1 of U22, a phase/frequency detector. The other input to U22 is the voltage-controlled oscillator's signal, which has been divided by 1000 in U19, 20, and 21 . U22 is a TTL device with negative edge-triggered inputs and active low outputs. Under normal, phase-locked operation, the negative edges at the inputs occur at the same moment, and the two outputs are high.

9E-4-19. Under these conditions, the two diodes following the detector (also part of U22) are back-biased and pass no current. Thus, no current enters the integrator amplifier, U23, and its output voltage remains fixed.

Figure 9E-4-1. Phase Detector Block Diagram


9E-4-20. Assume, however, the channel B frequency increases. This causes the negative pulses at U22(1) to arrive a little earlier than before, arriving ahead of the edges at pin 3. When an edge arrives at pin 1, the UP output goes active low and stays low until the edge at pin 3 arrives. The UP output, then, begins generating active low pulses. They are inverted into positive pulses that forward bias CRB Current flows through CRB and RBI into the integrator amplifier U23, causing the tuning voltage to integrate downward, this increases the PLO's output and increases the feedback frequency at U22 pin 3. This process continues until the signals at U22 pins 1 and 3 are again matched in frequency and phase. The phase detector outputs are inactive high, and the loop is at reset.

9E-4-21. THE VCO. The phase detector's corrections are integrated and saved by the loop amplifier, an integrating amplifier (refer to A1 schematic). This makes a tuning voltage that sets the frequency of the VCO (voltagecontrolled oscillator). The oscillator is a sawtooth generator driving a Schmitt trigger. The voltage-controlled current source (Q16) charges capacitor C42 in a linear ramp until the ramp reaches about +2.4 volts. This goes through an emitter follower (Q17) and appears at U24C(9) at +1.8 volts. This is the upper trigger point on the Schmitt gate. The output of U24C snaps low and discharges C42 through CR21 to about +0.6 volts. This is below the negative threshold, so the Schmitt output snaps high and the sawtooth starts again. The negative pulse train at $\mathrm{U} 24 \mathrm{C}(8)$ is used as the PLO output, and it is this frequency that is counted by the decade counter. The waveforms would appear as follows in Figure 9E-4-2.

Figure 9E-4-2. VCO Waveforms at about 2 MHz


9E-4-22. When the VCO is running at much less than 10 MHz , it becomes very difficult to see the narrow pulses at $\mathrm{U} 24 \mathrm{C}(8)$ on a scope. It is easier to see the sawtooth or a squarewave at half the VCO frequency at $\mathrm{U} 21(9)$.

It is important to remember that the frequency/tuning voltage (U23 pin 6) is a negative relationship. That is, the lower the tuning voltage, the higher the frequency.

9E-4-23. SEARCH INDICATOR CIRCUIT. The VCO runs continuously whether the loop is locked (normal operation) or not. In the high resolution range, there is always a signal from the VCO to the 5305B counting circuitry, whether or not there is a signal at the Channel B input. As a result, the counter is always armed and tries to count the VCO frequency (typically 25 kHz ) even without an input. This is prevented by the search indicator circuit, U16A, U18C, U24A, Q19.

9E-4-24. When the detector's inputs are in phase, the U22 output pins (2 and 13) are always TTL high. If the loop is not locked, one or the other will be low much of the time. Whenever either is low, $\mathrm{U} 24 \mathrm{~B}(6)$ is high, signaling an error in the loop. These error pulses are averaged by R77, R78, and C36. If the errors are large enough, the LOST line from comparator U16A will snap low in about 50 msec . In the high resolution range, LOST passes through gate U18C and becomes STOP (active high). STOP lights the SEARCH lamp (S annunciator) through Q14 and gates off the 10 MHz clock at U10A. This prevents the counter from continuing with more measurements or display updates and saves the last valid reading in the display. LOST enables gates U18 Band D, which pass the phase correction signals through resistor R76 ( $5.1 \mathrm{~K} \Omega$ ). R76 is in parallel with R 81 ( $82 \mathrm{~K} \Omega$, so R 81 is effectively reduced from 82 K ohm to about 5 K ohm. This greatly increases loop bandwidth and allows for rapid searches and short acquisition times.

9E-4-25. When an in-range input signal is restored to Channel B, the loop will lock. The phase corrections stop, $\mathrm{U} 24 \mathrm{~B}(6)$ (the error signal) stays low, and C36 discharges through R77 (100K ohm). As the average error signal drops through +1 volt, the lower threshold, U16A(1) snaps high, and STOP goes low. U24A(3) inverts this negative edge into a positive edge and Q19 is switched on for about 0.1 seconds. This clears all the counters and the time base, so a new measurement is begun, making the first reading correct. Meanwhile the SEARCH light is switched off, and the loop bandwidth is reduced to a low value to allow proper stability for accurate measurements.

9E-4-26. NON-LINEAR VCO CHARACTERISTIC. When no signal is available at the PLO input, the VCO is tuned to its lowest frequency (about 25 kHz ). If a high frequency (e.g., 10 kHz ) is applied, the VCO must be slewed all the way to $10,000 \mathrm{kHz}$ to achieve lock. This would take a loop that is stable at 50 Hz almost two minutes to achieve lock. Therefore, gates U18 Band D, were added, and the VCO characteristic has a bend in it. See Figure 9E-4-3.

9E-4-27. This puts a step in the loop bandwidth at about 300 Hz input, increasing loop bandwidth drastically above 300 Hz . (Higher loop bandwidth is allowable at higher input frequencies.) This greatly reduces acquisition time for a 10 kHz input.


Figure 9E-4-3. VCO Characteristics
9E-4-28. This "bend" in the VCO curve is accomplished by CR24 and R90. The corrected tuning voltage is the voltage between Q16's emitter and the +12 volt supply. The current is set by the resistance between those points. For corrected tuning voltages less than about 2 volts (actual tuning voltage above +9.4 vots), the tri-diode CR24 is off and out of the circuit. The effective resistance is R90 plus R83, or about 6500 ohms. For larger corrected tuning voltages (higher frequencies), the tri-diode is on and shorts R90, removing it from the circuit. Now the effective resistance is R83, or 270 ohms.

9E-4-29. MISCELLANEOUS A1 CIRCUITS. R84 (330K ohm) sets the lowest VCO frequency at about 25 kHz , corresponding to a 25 Hz input. Otherwise, the VCO could actually go to 0 Hz , and the phase detector outputs would go high because the VCO would appear to be locked to a 0 Hz input (no input). This would turn off the search indicator. C47 ( 100 pF ) sets the width of the narrow low-going pulse at the VCO output, U24C(8). Diode CR17 prevents the tuning voltage from going too low when the loop tries to acquire an excessive high input signal (above 11 kHz ). Pin 3 of the loop amplifier is biased at +1.5 volts. This forces pin 2 to also be at +1.5 V which is a good bias point to allow proper operation of the diode switches.

## 9E-4-30. $1300 \mathrm{MHz} \div$ Circuit

9E-4-31. The A2 board amplifies the Channel A signal and then divides it by four before sending it to A1 via J2(9). U26 divides the EECL signal by two before Q18 converts the signal to ECL. Another divide-by-two stage is provided by U25. Differential amplifier Q12 and Q13 converts the signal to a positive driven $\mathrm{ECL}(\mathrm{H}=4.3 \mathrm{~V}, \mathrm{~L}=3.2 \mathrm{~V})$. Q11 provides a low impedance to U12's input.

## 9E-4-32. A2 1300 MHZ AMPLIFIER ASSEMBLY

9E-4-33. The 1300 MHz Amplifier assembly (A2) consists of circuitry to amplify, prescale, and detect signals up to 1300 MHz . Input signals are routed through a protective fuse F1 to the 1.3 GHz limiter circuitry. The limiters consist of CR2, 7, 1, 4 and limit the input to approximately 5 V rms. A voltage controlled attenuator made up of pin diodes CR5, 6 , 8 , and 9 provides variable attenuation as determined by an AGC circuit.

9E-4-34. The attenuator output connects to U2 which provides $20-24 \mathrm{~dB}$ gain. U3 divides the signal by 2 and routes it to U4 where it is divided by 2 again. Since U3 has no Schmitt trigger, U3 is set for maximum sensitivity. Operating at maximum sensitivity gives U3 a tendency to oscillate when no input signal is applied. To maintain high sensitivity and prevent oscillations, R22 is adjusted to desensitize U3 when the ARM line is high and produce high sensitivity when the ARM line is low. This allows the counter readings to "snap on" from no input signal to the exact reading when a signal is applied. The amount of "snap on" feedback is controlled by A1R71. The greater the "snap on" feedback, the less the possibility of partial counts, but also the lower the sensitivity.

9E-4-35. As the input level increases, the level into A2U1 increases. A bridge circuit comprised of CR11, CR12 and associated resistors including R13 which balances the bridge. As the input level to the bridge increases, the rectified bridge output drives the input to U1 to cause the output of U1 to go low. This reduces the amount of current through A1R9, R1 (sen. pot), L4, L1, CR6, CR8, and R11. As the current through this path decreases, the resistance of the PIN diodes (CR6, 8) increases to offer more attenuation to the input signal. Similarly, the current increases through CR9, R10, R1, R3, R6, R7, CR5 and R2, which decreases the resistance of CR5 and CR9 to shunt more of the signal to ground through C5 and C9.

## SECTION IX E 5305B 1300 MHz COUNTER SUBSECTION V MAINTENANCE

## 9E-5-1 INTRODUCTION

9E-5-2. This subsection contains maintenance information for Model 5300/5305B 1300 MHz Counter. Performance checks, adjustment procedures, and tests to isolate defective components are included.

## 9E-5-3. RECOMMENDED TEST EQUIPMENT

9E-5-4. Test equipment recommended for performance checking and servicing the $5300 \mathrm{~B} / 5305 \mathrm{~B} 1300 \mathrm{MHz}$ Counter is listed in Table 9E-1-1 and in Table 5-1 in the 5300B portion of the manual. Test equipment with equivalent characteristics may be substituted for listed equipment.

## 9E-5-5. IN-CABINET PERFORMANCE CHECK

9E-5-6. Use the performance check in Table 9E-5-1 and the test card at the back of this subsection to verify proper operation of all circuits in the counter and all circuits in the 5300 that are used with the counter plug-on. The performance check may be used:
a. As part of an incoming inspection check of instrument specifications.
b. Periodically, for instruments used in systems where maximum reliability is important.
c. As part of a procedure to locate defective circuits.
d. After any repairs of adjustments, before returning instrument to regular service.

## 9E-5-7. INSTRUMENT ACCESS

9E-5-8. For access to the plug-on assembly, separate the 5300 from the 5305B as follows:
a. Turn ac power OFF and disconnect power cord.
b. Pull the two side casting latches fully rearward (it is necessary to press the latch handles gently away from the center of the instrument to unlock them).
c. When latches are fully extended rearward, the 5300 and 5305B cast housings should be separated by about $1 / 8$-inch.
d. Lift the 5300 gently away from the 5305B.
e. Separate 5305B Board Assembly from the cast housing as follows (refer to Figure 9E-5-1):
(1) Press rear, plastic-nylon retaining clips on each side of 5305B casting and lift the rear of the 5305B Assembly to release it from the casting.
(2) Press front plastic-nylon retaining clips on each side of 5305B casting and lift the front of the Board Assembly to release it from the casting.
(3) Lift Board Assembly from the casting.
f. Mate the 5305B Board Assembly to the 5300 and apply ac power.
g. To reinstall the Board Assembly into the casting, reverse procedure of steps $d$ through $f$.

## 9E-5-9. PERIODIC MAINTENANCE

$9 \mathrm{E}-5-10$. To determine if the $5300 \mathrm{~B} / 5305 \mathrm{~B}$ is operating properly, perform the In-Cabinet Performance Checks listed in Table 9E-5-1.

## 9E-5-11. MAINTENANCE AND REPAIR

9E-5-12. BOARD REMOVAL. When removing the printed circuit board for replacement, repair, or servicing, always remove ac power and separate the board from the casting according to Paragraph 9E-5-8, steps a to e.

9E-5-13. COMPONENT REPLACEMENT. When replacing a circuit board component, use a low heat soldering iron. Heat may be used sparingly as damage to the circuit foil may result. Mounting holes may be cleaned out with a toothpick while heat is applied. Connection should be cleaned with a cleaning solution after component removal and replacement.

## STEP A



STEP B

Table 9E-5-1. In-Cabinet Performance Check

## 1. SELF-CHECK

Perform Self-Check procedure, Figure 9E-3-3.

## 2. CHANNEL A

Obtain the following test equipment:
HP 8660B/86602A Synthesized Signal Generator
a. On 5305B, set RANGE to A, RESOLUTION to 1 K , and ATTENUATOR to MIN.
b. Set signal generator to 90 MHz at 20 mV .
c. Connect $8660 \mathrm{~B} / 86602 \mathrm{~A}$ Signal Generator output to 90 MHz to 1300 MHz jack of 5305 B .
d. Check frequencies in band of 90 to 1300 MHz . Counter should display selected frequency. count light (C) must be flashing.
e. Set ATTENUATOR control to MAX. Counter should stop counting.

## 3. CHANNEL B

Obtain the following test equipment:
HP 651B Test Oscillator
HP 8660B/86602A Synthesized Signal Generator
HP 11048A 50-ohm Feed-Thru Termination
a. On 5305B, set RANGE to B and RESOLUTION to 100 Hz .
b. Set 8660B/86602A Signal Generator to 100 MHz at 20 mV .
c. Connect signal generator output to 100 MHz jack of 5305 B using 50 -ohm feed-thru.
d. Check frequencies in band of 100 MHz down to 1 MHz . Counter should display selected frequency. count light (C) must be flashing.
e. Disconnect signal generator and connect a 651B test oscillator. Retain 50-ohm feed-thru.
f. On 5305B, set RESOLUTION to 1 kHz .
g. Set 651 B to 1 MHz at 20 mV .
h. Check frequencies in band of 1 MHz down to 50 Hz . Counter should display selected frequency. count light (C) must be flashing.
i. At 50 Hz , counter should display 00000.050 kHz .
j. On 5305B, set RANGE to B 10 kHz MAX.
k. The S light should light momentarily, and the display should read (display all 8's). counter should then display 00050.000 Hz . Count light (C) must be flashing.

## PERFORMANCE CHECK TEST CARD

Hewlett-Packard Model 5305B
1300 MHz COUNTER
Serial No.

1. SELF CHECK
2. CHANNEL A

Sensitivity ( 20 m V)
Frequency Range (90-1300 MHz)
Attenuator
3. CHANNEL B

Sensitivity ( 20 m V)
Frequency ( $50 \mathrm{~Hz}-100 \mathrm{MHz}$ )
High Resolution Mode

9E-5-14. INTEGRATED CIRCUIT REPLACEMENT. Two methods are recommended for removing integrated circuits:
a. Solder Gobbler. Solder is removed from board by a soldering iron with a hollow tip connected to a vacuum source. The IC is removed intact, so it may be reinstalled if diagnosis is wrong.
b. Clip Out. This method is used when an IC is proven defective. Clip leads close to case, apply heat, and remove leads with long-nose pliers. Clean board holes with toothpick and cleaning solution.

## 9E-5-15. ADJUSTMENTS

9E-5-16. The 5305B has two adjustments; Channel A and Channel B. The adjustments should be made when the incabinet performance test indicates the need, or when repairs are made which would affect the adjustment settings.

## 9E-5-17. CHANNEL A ADJUSTMENTS

9E-5-18. The following steps outline proper adjustment procedure for the Channel A amplfier.
a. On 5305B, set RANGE to A, RESOLUTION to 1 K , and ATTENUATOR to MIN.
b. Set A2R13 and A1R71 to full ccw. Set A2R22 to midrange.
c. Connect $5300 \mathrm{~B} / 5306 \mathrm{~A}$ voltmeter positive lead to J2 pin 5 and negative lead to rear panel.
d. Adjust A1R79 for 15 V reading.
e. Connect test setup shown in Figure 9E-5-2.

## NOTE

If meter on $8660 \mathrm{~B} / 86602 \mathrm{~A}$ is calibrated, a 3 foot coaxial cable may be substituted for the power splitter and power meter. Determine the drop in cable ( $\approx 1 \mathrm{~dB}$ ).
f. Set signal generator to 1.29999999 GHz at 30 mV .
g. Adjust A2R22 for maximum sensitivity. (Reduce signal level until A2R22 is adjusted for maximum sensitivity.)
h. Set signal generator to 90 MHz .
i. If counter displays a steading reading of 00270.XXX MHz , adjust output level of generator until counter reads 00268.XXX MHz.
j. Adjust A1R71 cw until count light (C) stops blinking.

Figure 9E-5-2. Test Setup for 1300 MHz Adjustment

k. Slowing increase generator output level and observe counter display. 5305B must never count 00180.XXX MHz . If it does, adjust A1R71 cw until count light (C) stops blinking.

1. Set generator output to 1.29999999 GHz at -21 dBm as measured on power meter. 5305B should count $01299.999 \mathrm{MHz} \pm 1$ count.
m . Reduce generator level until counter barely counts $01299.999 \mathrm{MHz} \pm 1$ count.
n. Adjust A1R71 cw if 5305B ambiguity is greater than $\pm 1$ count.
o. Adjust A2R22 cw until counter barely counts $01299.999 \mathrm{MHz} \pm 1$ count.
p. Input power to 5305 B should be $\leq-21 \mathrm{dBm}$.
q. Set input level to -21 dBm and scan frequency down to 90 MHz . Counter should display same reading as $8660 \mathrm{~B} \pm 1$ count.
r. Set generator to 90 MHz at -5 dBm .
s. Connect 5306A voltmeter to TPG and adjust A2R13 cw until 5306A reads $+12 \mathrm{~V} \pm 1 \mathrm{~V}$.

## 9E-5-19. CHANNEL B ADJUSTMENTS

9E-5-20. The following steps outline proper adjustment procedure for Channel B amplifier.
a. On 5305B, set RANGE to $B$ and RESOLUTION to 1 Hz .
b. Set A1R44 to midrange and A1R18 to full cw.
c. Set 651 B Test Oscillator to 50 Hz at 3 V rms and connect directly to 100 MHz input on 5305B.
d. Measure AGC voltage at TP A with $5300 \mathrm{~B} / 5306 \mathrm{~A}$ voltmeter.
e. Adjust A1R44 for 1.75 V reading on voltmeter.

## SERVICE NOTE

AGC voltage should decrease when level of input signal is reduced. With no input signal, AGC voltage should be negative.
f. Disconnect 651B Test Oscillator.
g. On 5305B, set RANGE to B and RESOLUTION to 10 Hz.
h. Set $8660 \mathrm{~B} / 86602 \mathrm{~A}$ Signal Generator to 100 MHz at $\pm 2 \mathrm{mV}$ and connect output of generator to 100 MHz input using a 50 -ohm feed-thru.
i. Increase generator level until counter displays stable 100 $\mathrm{MHz} \pm 1$ count.
J. Adjust A1R18 ccw to point where 5305B barely continues to make measurements. (Any more ccw rotation will stop gating.)
k. Input signal must be 20 mV or below.

## 9E-5-21. TROUBLESHOOTING

9E-5-22. Use the following troubleshooting information, the waveforms, and schematic diagram in subsection VIII to isolate troubles in the counter to a defective component.

9E-5-23. If a malfunction is suspected, operate the counter in the self-check mode (see Figure 9E-3-3) and analyze the front-panel indicators for evidence of improper operation. Some troubles can be quickly isolated in this manner. The self-check procedure exercises most of the counter circuits; the input circuits for both channels are notable exceptions.

9E-5-24. To determine if a problem exists in both input channels or just one channel, then perform the appropriate troubleshooting procedure. Be sure to perform each step in sequence. The following test equipment is required:
a. HP 10525T Logic Probe
b. HP 180A/1801A/1821A Oscilloscope
c. HP 651B Test Oscillator
d. HP 8660B/86602A Synthesized Signal Generator

## 9E-5-25. 100 MHz Channel Troubleshooting

9E-5-26. Set the RESOLUTION switch to 1 kHz and the RANGE switch to 100 MHz . Supply a 9 kHz , 1-volt sinusoidal input to the channel. Perform the following steps:
a. Check output of Channel B at A1 U6(3). See wave form photos in Subsection VIII.
b. If proper signal is not present, compare waveforms with those provided in Subsection VIII.
c. If proper signal is present, check operation of U12C.

## 9E-5-27. 1300 MHz Channel Troubleshooting

9E-5-28. Set the RESOLUTION switch to 1 kHz and the RANGE switch to A. Supply a 100 MHz , 25-millivolt, sinusoidal input to the channel and perform the following steps.
a. Check the EECL output of A2 at A2 pin 9 with oscilloscope. See waveform photos in Subsection VIII.
b. If signal is not present at pin 9 , check input fuse located in front panel jack before troubleshooting the assembly. See Subsection VIII for dc checks of A2.
c. If proper signal is present at A2 pin 9, check operation of U26, U25, Q13, Q11, and U12 on the A1 board.

## 9E-5-29. Both Channels Inoperative

9E-5-30. Set RESOLUTION switch to 1 Hz position, RANGE switch to B , and apply 1 MHz sinusoidal input to the 100 MHz channel from a 651 B Test Oscillator.
a. If any digit is blank, proceed to "Address Decoder Troubleshooting" in Paragraph 9E-5-31, also, at this time, note the unique displays listed under that heading.
b. If only the least-significant digit is displayed, and all others are zero, check U13C, Q9, and Q10.
c. If only decimal point problems occur, check A1P1 ( $34,46,47$, and 48 ) and the RESOLUTION switch.
d. Check U13A(5) for presence of input signal using oscilloscope. If not present, check operation of selected NOR gate responsible for passing signal.
e. Check for presence of clock signal at U10B(6). (U9A pin 3 should be a TTL low during gate time to pass clock pulses. If not, check input at U9A(1). If input does not toggle, continue to next step.
f. Using an osilloscope, check that U17 A(2) toggles (should be low ( $\approx 3.2 \mathrm{~V}$ ) during gate time). If not, check U17, Q7, and Q8.
g. With an oscilloscope, check that outputs of U14 are toggling (indication of counting).
h. Check that CLOSE line toggles. If not, check U17B, U16D, and CR10.
9E-5-31. ADDRESS DECODER TROUBLESHOOTING. Because the 5305B contains one decade of information, the Digit Address lines that strobe data onto the display must be altered to accommodate the extra digit. These code lines are altered in A1U4 and sent to the mainframe as Digit Select lines.

9E-5-32. The following table shows the display results when one of U4's outputs is stuck in one logic state. Important: set RESOLUTION switch to 10 kHz and use 1.25 MHz input signal.

Table 9E-5-2. Erroneous Displays Caused by U4

| Shorted to Ground | Display |
| :---: | :---: |
| $\Sigma 3($ pin 2) | 012.00120 MHz |
| $\Sigma 2($ pin 6) | 000.12120 MHz |
| $\Sigma 1$ (pin 9) | 000.00220 MHz |
| $\Sigma 4($ pin 15 ) | 000.00105 MHz |
| Stuck High | Display |
| $\Sigma 3$ (pin 2) | $000 . \mathrm{b} 0005 \mathrm{MHz}$ |
| $\Sigma 2$ (pin 6) | bbb.00005 MHz |
| $\Sigma 1$ (pin 9) | b00.00115 MHz |
| $\Sigma 4$ (pin 15) | 000.0012 b MHz |
| (or outputs of U5 |  |
| are inactive) |  |

b = blank
9E-5-33. Compare waveforms with those provided in Section VIII. $\Sigma 4$ should be low for $1 / 8$ of the period. The code for U4 appears as shown below.

| A3 | A2 | A1 | 4 | $\boldsymbol{\Sigma 3}$ | $\boldsymbol{\Sigma 2}$ | $\boldsymbol{\Sigma 1}$ |
| :--- | :---: | :---: | :--- | :--- | :--- | :--- |
| L | L | L | L | H | H | H |
| L | L | H | H | L | L | L |
| L | H | L | H | L | L | H |
| L | H | H | H | L | H | L |
| H | L | L | H | L | H | H |
| H | L | H | H | H | L | L |
| H | H | L | H | H | L | H |
| H | H | H | H | H | H | L |

## 9E-5-34. Frequency Multiplier

9E-5-35. Before troubleshooting the Frequency Multiplier circuit, ensure the Channel B amplifier is working properly. Apply a $1 \mathrm{kHz}, 100 \mathrm{~m}$ V signal to Channel B; set RANGE to B 10 kHz MAX and RESOLUTION to 1 Hz.
a. Check input to PLO at U22(1). It should be a 1 kHz square wave. If not, check $\mathrm{U} 16 \mathrm{~B}(2)$.
b. Check supplies: +5 V on $\mathrm{U} 21(14), \mathrm{U} 24(14) ;+$ 12 V at U23(7).
c. Check Loop Amplifier U23. For a 1 kHz input U23(6) should be about +9 V to +10 V . If it is +11 V or higher. the loop responds as if the VCO output frequency is too high. This is true when the loop is receiving no input at $\mathrm{U} 22(1)$. If $\mathrm{U} 23(6)$ is low (about +4 volts), the loop responds as if the VCO frequency is too low and tries to increase it. This would indicate a bad VCO (U24 pin 8) or a bad +1000 chain (U21, U20, and U19).

9E-5-36. VOLTAGE-CONTROLLED OSCILLATOR. To check the VCO, connect an oscilloscope to U21(9). There should be a square wave at half the VCO frequency. For a 1 kHz input, the VCO output should be $1 \mathrm{MHz}, 1000$ times the input. The VCO should run between 25 kHz (U236 at +11 volts) and 11 MHz (U23-6 at +4 volts). If a square wave appears at $U 21(9)$ from 12 kHz to 5.5 MHz , the frequency should relate roughly to the voltage at U23-6. If not, probably trouble is in circuitry associated with Q16, R83, C42 or U24(9) If there is no indication at U21-9, check the same circuits. Also, check that U23(6) is above +4 volts otherwise the VCO tends to shut off. CR17 should prevent this.

9E-5-37. If there is no indication at $\mathrm{U} 21(9)$, connect the scope to $\mathrm{U} 24(9)$ and check for the following sawtooth voltage at the output frequency.


9E-5-38. If the waveform is improper, the VCO is faulty. If the signal is present, and not at $\mathrm{U} 21(9)$, U 21 probably is bad. If the signal is between 12 kHz and 5 MHz at $\mathrm{U} 21(9)$, there should be $1 / 500$ of that at U22(3). If not, U19, U20 and/or U21 are faulty.

9E-5-39. DETECTOR CIRCUIT. Check U23(3); it should be at +1.5 volts $\pm .3$ volts. A problem here will saturate U 23 . Now test U22, the detector. If U22(1) has a faster frequency than U22(3), U22(13) should be low part or all of the time at a rate similar to that at U22(3).
$\mathrm{U} 22(2)$ should always be TTL high ( $\approx+3.5 \mathrm{~V}$ ).



9E-5-40. If the reserve is true, i.e., the frequency at U22(3) is greater than U22(1), then U22(2) should pulse low (or stay low if nothing at pin 1). and U22(13) should always be high. Pins 2 and 13 can never be low together.

9E-5-41. "S" LIGHT ON, NO MEASUREMENTS. It is possible that the PLO is working properly but no measurements are made. If the negative edges of the signals at U22(1 and 3) match, the PLO is working properly. U22(2 and 13 ) should be high almost all the time, and U24(6) should be low almost always (small positive pulses). Then U16(1) (LOST) should be at TTL 1 ( +5 volts). If not, there is something wrong with it or associated parts R77, R78, R59, R61, R62, R70 or R36. Also, U18(10) (STOP) should be low. When STOP is high, counter operation is frozen by stopping the clock at U10(13), and the "S" light is lit.

9E-5-42. FAULTY DISPLAYS WITH NO INPUT. When the input is disconnected, the display should hold last reading, if not check the stop circuits (U24, U16, and U18).

9E-5-43. INCORRECT FIRST READING. The first measurement after a signal is applied should be accurate. If way off, the reset circuit Q19 is not working. About $1 / 2$ second after a signal is applied, U18(10) should go low, U24(3) should go high and Q19 should saturate for about 1/10 second. This should pull J1-32 (50 pin connector) low and the system should "manual reset".

All 8's should show in the display (5300B) for $1 / 10$ second, then 0 's until new measurement displayed at end of gate time. If not, there are problems around Q19, R91, R92, R93 or C48.

9E-5-44. SLOW ACQUISITION. If the 5305B takes more than about 1 second to make a measurement (plus gate time) after a signal is applied to the PLO, circuit problems exist. With no signal, check that U16(1) is low (LOST). U18 should be enabled, and low rate ( 25 Hz ) pulses should appear at U18(1) (same as of U22 pin 2). Connect a 20 kHz signal and check for pulses at U18(13). The S light should be on during this time. Check CR16 and CR18. If CR24 (a tri-diode) is open, tuning will be slow and the upper frequency limit will be very low. If CR20 and CR22 are open, the loop will be slightly erratic.

9E-5-45. NOT ENOUGH RANGE ( 10 kHz ). If the PLO will not measure high frequencies ( 10 kHz ). check CR24 and ensure proper values of $\mathrm{R} 83, \mathrm{C} 42$, and C 47 . The tuning voltage at U23(6) should be about +4 V at the highest frequency. Also, check the +12 V supply.

9E-5-46. POOR LOW-FREQUENCY RANGE. If the counter does not measure low frequency ( 50 Hz ). check Q16 for leakage. Ensure that U23(6) can pull voltage high enough (Q16 should be off at the lower frequency limit). Check C49, C47, and CR21 and check for +1.5 V at U23(3).

# SECTION IX E <br> <br> 5305B 1300 MHz COUNTER <br> <br> 5305B 1300 MHz COUNTER <br> <br> SUBSECTION VI <br> <br> SUBSECTION VI <br> REPLACEABLE PARTS 

## 9E-6-1. INTRODUCTION

9E-6-2. This subsection contains information for ordering replacement parts. Table 9E-6-1 lists parts used in the standard counter in alphanumeric order of their reference designators and provides the following information for each part. Table 9E-6-2 lists parts used in Option 001. Miscellaneous parts are listed at the end of Table 9E-6-1.
a. Hewlett-Packard part number.
b. Description of part (see abbreviation below).
c. Total quantity used in the instrument (shown only after the first entry for a given part).
d. Typical manufacturer of the part in a five-digit code (see list of manufacturer's in Table 9E-6-2).

## 9E-6-3. ORDERING INFORMATION

9E-6-4. To obtain replacement parts, address order to your local Hewlett - Packard Sales and Service Office (see lists in section VI of the 5300 manual for addresses). Identify parts by their Hewlett-Packard part number. To obtain a part that is not listed, include:
a. Instrument model number.
b. Instrument serial number.
c. Description of the part.
d. Function and location of the part.
e. Manufacturer's part number.

| REFERENCE DESIGNATIONS |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| A | = assembly | E | = miscellaneous electrical | MP | = miscellaneous | TP | = test point |
| AT | = attenuator; isolator; termination | F | part $=$ fuse |  | mechanical part = electrical connector | U | $=$ integrated circuit; microcircuit |
| B | = fan; motor | FL | = filter |  | (movable portion); | V | = electron tube |
| BT | = battery | H | = hardware |  | plug | VA | = voltage regulator; |
| C | = capacitor | HY | = circulator | Q | = transistor; SCR; triode |  | breakdown diode |
| CP | = coupler | J | = electrical connector |  | thyristor | W | = cable; transmission |
| CA | = diode; diode thyristor; |  | (stationary portion); | ${ }^{\text {A }}$ | = resistor |  | path; wire |
|  |  |  |  |  | = thermistor | X | = socket |
| DC | = directional coupler |  |  | S | = switch | Y | = crystal unit-piezo- |
| DL | = delay line | K | = relay | T | = transformer |  | electric |
| DS | = annunciator; signaling device (audible or visual); lamp; LED | $\begin{aligned} & \mathrm{L} \\ & \mathrm{M} \end{aligned}$ | = coil; inductor <br> $=$ meter | $\begin{aligned} & \text { TB } \\ & \text { TC } \end{aligned}$ | = terminal board <br> = thermocouple | Z | $=\underset{\text { circuit }}{\text { tuned cavity; tuned }}$ |
| ABBREVIATIONS |  |  |  |  |  |  |  |
| A | = ampere | BCD | = binary coded decimal | COMP | = composition | ${ }^{\circ} \mathrm{K}$ | = degree Kelvin |
| ac | = alternating current | BD | = board | COMPL | = complete | DEPC | = deposited carbon |
| ACCESS | = accessory | BECU | = beryllium copper | CONN | = connector | DET | = detector |
| ADJ | = adjustment | BFO | = beat frequency | CP | = cadmium plate | diam | = diameter |
| AID | = analog-to-digital |  | oscillator | CAT | = cathode-ray tube | DIA | = diameter (used in |
| AF | = audio frequency | BH | = binder head | CTL | = complementary tran- |  | parts list) |
| AFC | = automatic frequency | BKDN | = breakdown |  | sistor logic | DIFF |  |
|  | control | BP | = bandpass | CW | = continuous wave | AMPL | = differential amplifier |
| AGC | = automatic gain control | BPF | = bandpass filter | cw | = clockwise |  | = division |
| AL | = aluminum | BAS | = brass | D/A | = digital-to-analog | DPDT | = double-pole,double- |
| ALC | = automatic level control | BWO | = backward-wave | dB | = decibel |  | throw |
| AM | = amplitude modulation |  | oscillator | dBm | = decibel referred to | DA | = drive |
| AMPL | = amplifier | CAL | = calibrate |  | 1 mW | DSB | = double sideband |
| APC | $\begin{aligned} & =\text { automatic phase } \\ & \text { control } \end{aligned}$ | $\begin{aligned} & \text { CcW } \\ & \text { CEA } \end{aligned}$ | = counterclockwise <br> = ceramic | dc deg | = direct current <br> = degree (temperature | DTL DVM | = diode transistor logic <br> = digital voltmeter |
| ASSY | = assembly | CHAN | = channel |  | interval or difference) | ECL | = emitter coupled logic |
| AUX | = auxiliary | cm | = centimeter | $\bigcirc$ | = degree (plane angle) | EMF | = electromotive force |
| avg | = average | CMO | = coaxial | ${ }^{\circ} \mathrm{C}$ | = degree Celsius | EDP | = electronic data |
| AWG | = american wire gauge | COEF | = coefficient |  | (centrigrade) |  | processing |
| BAL | = balance | COM | = common | ${ }^{\circ} \mathrm{F}$ | = degree Fahrenheit | ELECT | = electrolytic |


|  | abbreviations (CONTINUED) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | = encapsulate <br> = external $=$ farad <br> = field-effect transistor <br> $=$ flip-flop $=$ flat head <br> = fillister head <br> $=$ frequency modulation $=$ front panel <br> $=$ frequency $=$ fixed <br> $=$ fixed $=$ gram <br> = germanium <br> = gigahertz = glass <br> $=$ ground $($ ed $)$ $=$ Henry <br> $=$ hour <br> $=$ heterodyne $=$ hexagonal <br> $=$ head $=$ hardware <br> $=$ high frequency <br> $=$ mercu $=$ high <br> = Hewlett-Packard <br> = hour (used in parts list) <br> $=$ high V $=$ Hertz <br> = integrated circuit <br> $=$ intermediate frequency $=$ impregnated <br> = inch <br> $=$ inc <br> = include(s) <br> $=$ include $(s)$ $=$ input <br> $=$ insulation $=$ internal <br> $=$ kilogram = kilohertz <br> = kilohm <br> $=$ kilovol <br> $=$ pound $=$ inductance-capacitance <br> $=$ light-emitting diode $=$ low frequency <br> $=$ long $=$ left hand <br> $=$ limit $=$ linear <br> parts list) (used in <br> $=$ linear <br> = low; local oscillator <br> $\begin{aligned}= & \text { logarithmic taper } \\ & \text { (used in parts list) }\end{aligned}$ <br> $=$ logarithm(ic) $=$ low pass filter <br> = low voltage <br> $=$ milliampere <br> $=$ megohm <br> $=\underset{\text { parts list) }}{ }\left(10^{6}\right)$ (used in <br> $=$ metal film <br> $=$ metal oxide $=$ medium frequency; <br> parts list) <br> = milligracm <br> $=$ megahertz $=$ millihenry <br> $=$ mho $=$ minimum | $\min$ $\ldots$ <br> MINAT <br> MOD MOM <br> MOS <br> MS MTG MTR <br> mV <br> mVdc <br> $m V p k$ $m V p-p$ <br> mVrms <br> mW MUX <br> MY $\mu \mathrm{A}$ <br> $\mu \mathrm{F}$ $\mu \mathrm{H}$ <br> ${ }_{\text {ush }}^{\text {unc }}$ <br> ${ }^{\mathrm{HN}} \mathrm{V}$ Vac <br> $\mu \mathrm{Vdc}$ $\mu \mathrm{Vpk}$ <br> $\mu \vee p-p$ <br> $\mu \mathrm{Vrms}$ $\mu \mathrm{W}$ <br>  <br>  <br> NEG <br> Nin <br> Nip <br> No NOM <br> NoM NoRM NPN <br> npo <br> NRFR <br> NSR <br> ns nW <br>  <br> OH <br> OPT OSC <br> OX <br> OZ $\Omega$ P <br> PAM <br> ${ }_{\mathrm{PCM}}^{\mathrm{PC}}$ <br> PDM <br> ${ }_{\mathrm{PH}}^{\mathrm{pH}} \mathrm{BRZ}$ <br> PHL PIN | $=$ minute (time) $=$ minute (plane angle) <br> $=$ miniature <br> = modulator <br> $=$ momentary $=$ metal-oxide semi- <br> $=$ millisecond <br> $=$ mounting $=$ meter (indicating <br> device) $=$ millivolt <br> $=$ millivolt $=$ millivolt, ac <br> $=$ millivolt, dc $=$ millivolt, <br> $=$ millivolt, peak-to-peak $=$ millivolt, rms <br> $=$ milivivot, $=$ milliwatt = multiplex <br> $=$ multiplex $=$ mylar <br> $=$ microampere $=$ microfarad <br> = microhenry <br> $=$ microsecond <br> $=$ microvolt, ac <br> $=$ microvolt, dc <br> $=$ microvolt, peak <br> $\begin{aligned} & \text { peak } \\ = & \text { micro }\end{aligned}$ <br> = microwatt <br> = nanoampere <br> $=$ normally closed $=$ neon <br> = negative <br> $=$ nanofarad $=$ nickel plate <br> $=$ normally open $=$ nominal <br> = normal <br> negative-positlve- <br> (zero temperature <br> $=$ not recommended for <br> field replacement $=$ not separately <br> = nanosecond <br> = nanowatt <br> = outside diameter <br> = oval head $=$ operational amplifier <br> $=$ option $=$ oscillator <br> = oxide <br> $=$ ounce $=$ ohm <br> $=$ peak (used in parts <br> = pulse-amplitude <br> $=$ modulation <br> $=$ pulse-code modulation; pulse-count modulation <br> $=$ pulse-duration <br> $=$ picofarad <br> = phosphor bronze <br> = positive-instrinsic- | PIV <br> $\underset{\substack{\text { 想 } \\ \text { p.o }}}{ }$ <br> ${ }_{\text {PNP }}^{\text {PN }}$ <br> por pors <br> poss <br> ${ }_{p}^{p, p}$ <br> ppM <br> ${ }^{\text {PREAREAPL }}$ <br> PRR <br> $\stackrel{\substack{\text { ps } \\ \hline \text { pT }}}{ }$ <br> ${ }^{\text {Prom }}$ <br> $\underset{\substack{\text { PRO } \\ \text { Rect }}}{ }$ <br> Rect <br> Rect <br> REG <br>  <br> ${ }_{\text {RFI }}^{\text {RF }}$ <br> $\underset{\text { RLC }}{\text { R.C }}$ <br> RMo <br> $\underset{\substack{\text { mis } \\ \text { mo }}}{\substack{20 M}}$ <br> $\substack{\text { Rese } \\ \text { RNW }}$ <br> S S <br> S-B <br> SCR <br> ${ }_{\text {sect }}^{\text {sect }}$ <br> Semicon <br> $\underset{\substack{\text { sil } \\ \text { sil }}}{ }$ <br> $\underset{\substack{\text { st } \\ \text { sppor }}}{ }$ <br> $\underset{\substack{\text { spe } \\ \text { spst }}}{\substack{\text { sen }}}$ <br> ssb <br> ssit <br> sct <br> sid <br> swion <br> swion <br>  <br> $\stackrel{\text { TA }}{\text { TA }}$ <br> ${ }_{\text {TERM }}$ | = peak inverse voltage <br> = phase lock <br> = phase lock oscillator <br> $=$ positive-negative- <br> positive $=$ part of <br> $=$ polystyrene $=$ porcelain <br> $=\begin{aligned} & \text { positive; position(s) } \\ & \text { (used in parts list) }\end{aligned}$ <br> $=$ position $=$ potentiometer <br> $=$ peak-to-peak $=$ peak-to-peak (used in <br> parts list) $=$ pulse-position <br> $=$ modulation <br> $=$ pulse-repetition frequency <br> $=$ pulse repetition rate $=$ picosecond <br> $=$ point $=$ pulse-time modulation <br> $=$ pulse-width modulation $=$ peak working voltage <br> = peak working voltage $=$ resistance capacitance <br> = rectifier $=$ reference <br> $=$ regulated $=$ replaceable <br> $=$ radio frequency $=$ radio frequency <br> interference <br> $=$ round head; right hand $=$ resistance-inductance- <br> $=$ capacitance <br> = root-mean-square <br> $=$ round $=$ read-only memory <br> $=$ rack and panel $=$ reverse working voltage = scattering parameter <br> $=$ second (time) $=$ second (plane a <br> $=$ slow-blow (fuse (used in parts list) <br> = silicon controlled <br> $\begin{aligned} & \text { rectifier; screw } \\ = & \text { selenium }\end{aligned}$ <br> $=$ sections $=$ semiconducto <br> $=$ superhigh frequency $=$ Silicon <br> $=$ silver $=$ slide <br> $=$ signal-to-noise ratio $=$ single-po <br> $=\underset{\text { throw }}{\text { single-pole, double }}$ <br> $=$ spring $=$ split ring <br> = single-pole. single- <br> $=$ single sideband $=$ stainless steel <br> = steel $=$ squar <br> = square = standing <br> $=$ synchronize $=$ timed (slow-blow fuse) <br> $=$ tantalum $=$ temperature <br> $\quad$ compensating $=$ time delay <br> = terminal |  | = thin-film transisto <br> $=$ toggle $=$ thread <br> $=$ through $=$ titanium <br> = tolerance <br> $=$ trimmer $=$ transistor <br> $=$ transistor-transistor <br> logic $=$ television <br> $=$ television interference = traveling <br> $=$ traveling wave tube $=$ micro $\left(10^{-6}\right)$ (used in <br> parts list) $=$ microfarad <br> $=$ microfarad (used in parts list) <br> $\begin{aligned} & \text { parts list) } \\ = & \text { ultrahigh }\end{aligned}$ <br> $=$ unregulated $=$ volt <br> $=$ volt $=$ volta <br> = voltampere = volts ac <br> = variable = voltage-con <br> $=$ voltage-controlled <br> $=$ volts dc <br> $=$ volts dc, working (used <br> $=$ in parts list) <br> = variable-frequency <br> $=$ very-high frequency <br> $=$ Volts peak $=$ Volts peak-to-peak <br> $=\mathrm{VO}$ <br> $=$ volta <br> - <br> = vacuum-tube voltmeter <br> $=$ volts. Switched $=$ watt <br> = with <br> = wirewing inverse voltage <br> $=$ without $=$ yttrium-ir <br> = characteris-garnet <br> impedance <br> NOTE Nreviations in the parts <br> be in upper case. <br> MULTIPLIERS <br> iation <br>  |

Model 5305B

Table 9E-6-1. Replaceable Parts

| Reference Designation | HP Part Number | Qty | Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A1 | 05305-60005 | 1 | BOARD ASSY, LOGIC | 28480 | 05305-60005 |
| A1C1 | 0160-4084 | 13 | CAPACITOR-FXD . $1 \mathrm{UF}+-20 \%$ 50WVDC CER | 28480 | 0160-4084 |
| A1C3 | 0160-4084 | 3 | CAPACITOR-FXD 15UF +-10\% 20VDC TA | 58289 | 1500156X9020B2 |
| A1C4 | 0150-0075 | 1 | CAPACITOR-FXD 4700PF +100-0\% 500WVDC CER | 28480 | 0150-0075 |
| A1C5 | 0160-3879 | 11 | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C6 | 0160-3879 |  | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C7 | 0160-3456 | 1 | CAPACITOR-FXD 1000PF +-10\% 1000WVDC CER | 28480 | 0160-3456 |
| A1C8 | 0160-4084 |  | CAPACITOR-FXD . $1 \mathrm{UF}+$ +-20\% 50WVDC CER | 28480 | 0160-4084 |
| A1C9 | 0180-1746 |  | CAPACITOR-FXD 15UF +-10\% 20VDC TA | 56289 | 150D156X9020B2 |
| A1C10 | 0180-1746 |  | CAPACITOR-FXD 15UF +-10\% 20VDC TA | 56289 | 150D156X9020B2 |
| A1C11 | 0180-0155 | 2 | CAPACITOR-FXD 2.2UF +-20\% 20VDC TA | 56289 | 150D225X0020A2 |
| A1C12 | 0160-3879 |  | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C13 A1C14 | $0180-0210$ $0180-1701$ | 3 4 | CAPACITOR-FXD $3.3 \mathrm{UF}+$ +20\% 15VDC TA CAPACITOR-FXD $6.8 \mathrm{UF}+20 \%$ 6VDC TA | 56289 56289 | 150D335X0015A2 150D685X0006A2 |
| A1C15* | 0160-3873 | 2 | CAPACITOR-FXD 4.7PF +-.5PF 200WVDC CER | 28480 | 0160-3873 |
| A1C15* | 0160-3872 |  | CAPACITOR-FXD 2.2PF +-.25PF 200WVDC CER | 28480 | 0160-3872 |
| A1C15* | 0160-3874 |  | CAPACITOR-FXD 10PF +-.5PF 200WVDC CER | 28480 | 0160-3874 |
| A1C16 | 0160-3879 |  | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C17 | 0180-0490 | 3 | CAPACITOR-FXD 68UF +-10\% 6VDC TA | 56289 | 196D686X9006KA1 |
| A1C18 | 0180-0553 | 1 | CAPACITOR-FXD 22UF +-20\% 25VDC TA | 28480 | 0180-0553 |
| A1C19 | 0160-4084 |  | CAPACITOR-FXD . $1 \mathrm{UF}+$ +20\% 50WVDC CER | 28480 | 0160-4084 |
| A1C20 | 0180-0210 |  | CAPACITOR-FXD 3.3UF +-20\% 15VDC TA | 56289 | 150D335X0015A2 |
| A1C21 | 0180-1702 | 2 | CAPACITOR-FXD 180UF +-20\% 6VDC TA | 56289 | 150D187X0006R2 |
| A1C22 A1C23 | 0180-1702 |  | CAPACITOR-FXD 180UF +-20\% 6VDC TA | 56289 | 150D187X0006R2 |
| A1C23 | $0180-0210$ $0180-0490$ |  | CAPACITOR-FXD $3.3 \mathrm{UF}+\mathrm{+}$-20\% 15VDC TA CAPACITOR-FXD $68 \mathrm{UF}+-10 \%$ 6VDC TA | 56289 56289 | 150D335X0015A2 196D686X9006KA1 |
| A1C25 | 0180-1701 |  | CAPACITOR-FXD 6.8UF +-20\% 6VDC TA | 56289 | 150D685X0006A2 |
| A1C26 A1C27 | 0180-0490 $0160-4084$ |  | CAPACITOR-FXD 68UF +-10\% 6VDC TA CAPACITOR-FXD . 1UF +-20\% 50WVDC CER | 56289 28480 | $\begin{aligned} & \text { 196D686x9006KA1 } \\ & 0160-4084 \end{aligned}$ |
| A1C28 | 0160-4084 |  | CAPACITOR-FXD . $1 \mathrm{UF}+\mathrm{+} 20 \% 50 \mathrm{WVDC}$ CER | 28480 | 0160-4084 |
| A1C29 | 0160-4084 |  | CAPACITOR-FXD . $1 \mathrm{UF}+-20 \% 50 W V D C$ CER | 28480 | 0160-4084 |
| A1C30 | 0160-4084 |  | CAPACITOR-FXD . $1 \mathrm{UF}+$ +20\% 50WVDC CER | 28480 | 0160-4084 |
| A1C31 | 0160-3879 |  | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C32 | 0160-3879 |  | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C33 | 0160-3879 |  | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C34 | 0160-3879 |  | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C35 | 0180-0098 | 1 | CAPACITOR-FXD 100UF +-20\% 20VDC TA | 56289 | 150D107X0020S2 |
| A1C36 | 0180-1701 |  | CAPACITOR-FXD 6.8UF +-20\% 6VDC TA | 56289 | 150D685X0006A2 |
| A1C37 | 0160-4084 |  | CAPACITOR-FXD . $1 \mathrm{UF}+$ +20\% 50WVDC CER | 28480 | 0160-4084 |
| A1C38 | 0160-3879 |  | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C39 | 0160-4084 |  | CAPACITOR-FXD . $1 \mathrm{UF}+$ +20\% 50WVDC CER | 28480 | 0160-4084 |
| A1C40 | 0180-0374 | 1 | CAPACITOR-FXD 10UF +-10\% 20VDC TA | 56289 | 150D106X9020B2 |
| A1C41 A1C42 | $0160-4084$ $0140-0178$ |  | CAPACITOR-FXD . $1 \mathrm{UF}+-20 \%$ 50WVDC CER CAPACITOR-FXD 560PF $+-20 \%$ 300WVDC MICA | 28480 |  |
| A1C42 A1C43 | $0140-0178$ $0160-4084$ | 1 | CAPACITOR-FXD 560PF +-20\% 300WVDC MICA CAPACITOR-FXD . $1 \mathrm{UF}+\mathrm{20} \mathrm{\%} 50 \mathrm{WVDC} \mathrm{CER}$ | 72136 28480 | DM15F561G0300WVICR 0160-4084 |
| A1C44 | 0160-3879 |  | CAPACITOR-FXD . 01 UF + - $20 \%$ 100WVDC CER | 28480 | 0160-3879 |
| A1C45 | 0160-3879 |  | CAPACITOR-FXD .01UF +-20\% 100WVDC CER | 28480 | 0160-3879 |
| A1C46 | 0180-0155 |  | CAPACITOR-FXD 2.2UF+-20\% 20VDC TA | 56289 | 150D225X0020A2 |
| A1C47 | 0160-2204 | 1 | CAPACITOR-FXD 100PF+-5\% 300WVDC MICA | 09023 | RDM15F101J3C |
| A1C48 | 0180-1101 |  | CAPACITOR-FXD 6.8UF+-20\% 6VDC TA | 56289 | 150D685X0006A2 |
| A1C49 | 0160-4084 |  | CAPACITOR-FXD . $1 \mathrm{UF}+$ +-20\% 50WVDC CER | 28480 | 0160-4084 |
| A1C50 | 0180-0195 | 1 | CAPACITOR-FXD .33UF+-20\% 35VDC TA | 56289 | 150D334X0035A2 |
| A1CR1 | 1910-0016 | 4 | DIODE-GE 60V 60NA 1US DO-7 | 28480 | 1910-0016 |
| A1CR2 | 1901-0119 | 7 | DIODE-SWITCHING 15V 50MA 750PS DO-7 | 28480 | 1901-0119 |
| A1CR3 | 1902-0025 | 1 | DIODE-ZNR 10V 5\% DO-7 PD=.4W TC=+.06\% | 04713 | SZ 10939-182 |
| A1CR4 | 1901-0119 |  | DIODE-SWITCHING 15V 50MA 750PS DO-7 | 28480 | 1901-0119 |
| A1CR5 | 1901-0179 |  | DIOOE-SWITCHING 15V 50MA 750PS DO-7 | 28480 | 1901-0179 |
| A1CR6 | 1901-0040 | 8 | DIODE-SWITCHING 30V 50MA 2NS DO-35 | 28480 | 1901-0040 |
| A1CR7 | 1901-0179 |  | DIODE-SWITCHING 15V 50MA 750PS DO-7 | 28480 | 1901-0119 |
| A1CR8 | 1901-0040 |  | DIODE-SWITCHING 30V 50MA 2NS DO-35 | 28480 | 1901-0040 |
| A1CR9 | 1901-0535 | 5 | DIODE-SCHOTTKY | 28480 | 1901-0535 |
| A1CR10 | 1910-0016 |  | DIODE-GE 60V 60NA 1US DO-7 | 28480 | 1910-0016 |
| A1CR11 | 1901-0535 |  | DIODE-SCHOTTKY | 28480 | 1901-0535 |
| A1CR12 | 1901-0535 |  | DIODE-SCHOTTKY | 28480 | 1901-0535 |
| A1CR13 | 1901-0535 |  | DIODE-SCHOTTKY | 28480 | 1901-0535 |
| A1CR14 | 1901-0028 | 2 | DIODE-PWR RECT 400V 750 MA DO-29 | 04713 | SR1358-9 |
| A1CR15 | 1901-0040 |  | DIODE-SWITCHING 30V 50MA 2NS DO-35 | 28480 | 1901-0040 |
| A1CR16 | 1901-0040 |  | DIODE-SWITCHING 30V 50MA 2NS DO-35 | 28480 | 1901-0040 |
| A1CR17 | 1901-0040 |  | DIODE-SWITCHING 30V 50MA 2NS DO-35 | 28480 | 1901-0040 |
| A1CR18 | 1901-0040 |  | DIODE-SWITCHING 30V 50MA 2NS DO-35 | 28480 | 1901-0040 |
| A1CR19 | 1901-0040 |  | DIODE-SWITCHING 30V 50MA 2NS DO-35 | 28480 | 1901-0040 |
| A1CR20 | 1910-0016 |  | DIODE-GE 60V 60NA 1US DO-7 | 28480 | 1910-0016 |

Table 9E-6-1. Replaceable Parts (Cont'd)

| Reference Designation | HP Part Number | Qty | Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A1CR21 | 1901-0535 |  | DIODE-SCHOTTKY | 28480 | 1901-0535 |
| A1CR22 | 1910-0016 |  | DIODE-GE 60V 60NA 1US DO-7 | 28480 | 1910-0016 |
| A1CR23 | 1901-0028 |  | DIODE-PWR RECT 400V 750MA DO-29 | 04713 | SR1358-9 |
| A1CR24 | 1901-0460 | 1 | DIODE- STABISTOR 15V 150MA DO-7 | 28480 | 1901-0460 |
| A1J1 | 1251-4277 | 1 | CONNECTOR 10-PIN F POST TYPE | 28480 | 1251-4277 |
| A1L1 | 9100-1633 | 3 | COIL-MLD 68UH 5\% Q=55.155DX.375LG | 24226 | 15/682 |
| A1L2 | 9100-2256 | 1 | COIL-FXD MOLDED RF CHOKE . 56 UH 10\% | 24226 | 10/560 |
| A1L3 | 9100-1633 |  | COIL-MLD 68UH 5\% Q=55.155DX.375LG | 24226 | 15/682 |
| A1L4 A1L5 | $9100-1633$ $9100-3139$ | 2 | COIL-MLD 68UH 5\% Q=55.155DX.375LG COIL-FXD NON-MOLDED RF CHOKE 75UH 15\% | 24226 | 15/682 $9100-3139$ |
| A1L6 | 9100-3139 |  | COIL-FXD NON-MOLDED RF CHOKE 75UH 15\% | 28480 | 9100-3139 |
| A1L7 | 9100-0346 | 1 | COIL-FXD MOLDED RF CHOKE . 05 UH 20\% | 28480 | 9100-0346 |
| A1P1 | 1251-2756 | 1 | CONNECTOR 50-PIN F MICRO RIBBON | 71785 | 222-22-50-069 |
| A1Q1 | 1855-0081 | 1 | TRANSISTOR J-FET 2N5245 N-CHAN D-MODE SI | 01295 | 2N5245 |
| A1Q2 | 1853-0247 | 1 | TRANSISTOR PNP SI PD=200MW FT=1.5GHZ | 28480 | 1853-0247 |
| A1Q3 | 1855-0386 | 1 | TRANSISTOR J-FET 2N4392 N-CHAN D-MODE | 04713 | 2N4392 |
| A1Q4 | 1854-0071 | 5 | TRANSISTOR NPN SI PD=300MW FT-200MHZ | 28480 | 1854-0071 |
| A1Q5 | 1853-0015 | 4 | TRANSISTOR PNP SI PD=200MW FT=500MHZ | 28480 | 1853-0015 |
| A1Q6 | 1854-0634 | 1 | TRANSISTOR NPN SI PD=1W FT=50MHZ | 04713 | MPS-UO1 |
| A1Q7 | 1854-0071 |  | TRANSISTOR NPN SI PD $=300 \mathrm{MW}$ FT $=200 \mathrm{MHZ}$ | 28480 | 1854-0071 |
| A1Q8 | 1853-0015 |  | TRANSISTOR PNP SI PD=200MW FT $=500 \mathrm{MHZ}$ | 28480 | 1853-0015 |
| A1Q9 A1Q10 | $1853-0015$ $1853-0015$ |  | TRANSISTOR PNP SI PD $=200 \mathrm{MW}$ FT $=500 \mathrm{MHZ}$ TRANSISTOR PNP SI PD $=200 \mathrm{MW} \mathrm{FT}=500 \mathrm{MHZ}$ | 28480 | $1853-0015$ $1853-0015$ |
| A1Q11 | 1854-0009 | 4 | TRANSISTOR NPN 2N709 SI TO-18 PD=300MW | 2B480 | 1854-0009 |
| A1Q12 | 1854-0009 |  | TRANSISTOR NPN 2N709 SI TO-18 PD=300MW | 28480 | 1854-0009 |
| A1Q13 | 1854-0009 |  | TRANSISTOR NPN 2N709 SI TO-18 PD=300MW | 28480 | 1854-0009 |
| A1Q14 | 1854-0071 |  | TRANSISTOR NPN SI PD=300MW FT=200MHZ | 28480 | 1854-0071 |
| A1Q15 | 1853-0036 | 2 | TRANSISTOR. PNP SI PD=310MW FT=250MHZ | 04713 | SPS-3612 |
| A1Q16 | 1853-0036 |  | TRANSISTOR. PNP SI PD=310MW FT=250MHZ | 04713 | SPS-3612 |
| A1Q17 | 1854-0071 |  | TRANSISTOR NPN SI PD $=300 \mathrm{MW} \mathrm{FT}=200 \mathrm{MHZ}$ | 28480 | 1854-0071 |
| A1Q18 | 1854-0009 |  | TRANSISTOR NPN 2N709 SI TO-18 PD=300MW | 28480 | 1854-0009 |
| A1Q19 | 1854-0071 |  | TRANSISTOR NPN SI PD=300MW FT= 200 MHZ | 28480 | 1854-0071 |
| A1R1 | 2100-3434 |  | RESISTOR-VAR CONTROL CC 50K 10\% LIN | 01121 | 70M4N048P503U |
| A1R2 | 0698-8339 | 1 | RESISTOR 82K 5\% .125W CC TC=-466/+875 | 01121 | BB8235 |
| A1R3 | 0698-7964 | 2 | RESISTOR 100K $5 \% .125 \mathrm{~W}$ CC TC=-466/+875 | 01121 | BB1045 |
| A1R4 | 0698-5176 |  | RESISTOR $5105 \% .125 \mathrm{~W}$ CC TC=-330/+800 | 01121 | BB5115 |
| A1R5 | 0698-7097 |  | RESISTOR 1M 5\%.125W CC TC=-600/+1137 | 01121 | BB1055 |
| A1R6 | 0698-5564 | 1 | RESISTOR $2405 \% .125 \mathrm{~W}$ CC TC= $=300 /+800$ | 01121 | BB2415 |
| A1R7 | 0683-2015 | 4 | RESISTOR $2005 \% .25 \mathrm{~W}$ FC TC=-400/+600 | 01121 | CB2015 |
| A1R8 A1R9 | $0698-5175$ $0683-2015$ | 6 | RESISTOR $3605 \% .125 \mathrm{~W}$ CC TC $=-330 /+800$ RESISTOR $2005 \% .25 \mathrm{~W}$ FC TC $=-400 /+600$ | 01121 | BB3615 |
| A1R9 A1R10 | 0683-2015 |  | RESISTOR $2005 \% .25 \mathrm{~W}$ FC TC $=-400 /+600$ RESISTOR $100 \mathrm{~K} 5 \% .125 \mathrm{~W}$ CC TC $=-466 /+875$ | 01121 01121 | CB2015 |
| A1R11 | 0698-5176 |  | RESISTOR $5105 \% .125 \mathrm{~W}$ CC TC $=-330 /+800$ | 01121 | BB5115 |
| A1R12 | 0698-5180 | 6 | RESISTOR 2K 5\%.125W CC TC=-350/+857 | 01121 | BB2025 |
| A1R13 | 0698-5185 | 1 | RESISTOR 15K $5 \% .125 \mathrm{~W}$ CC TC=-466/+875 | 01121 | BB1535 |
| A1R14 | 0698-5175 |  | RESISTOR $3605 \%$.125W CC TC=-330/+800 | 01121 | BB3615 |
| A1R15 | 0698-5180 |  | RESISTOR 2K 5\% . 125 W CC TC=-350/+857 | 01121 | BB2025 |
| A1R16 | 0698-5180 |  | RESISTOR 2K 5\%.125W CC TC=-350/+857 | 01121 | BB2026 |
| A1R17 | 0698-5180 |  | RESISTOR $2 \mathrm{~K} 5 \% .125 \mathrm{~W}$ CC TC=-350/+857 | 01121 | BB2025 |
| A1R18 | 2100-1738 | 3 | RESISTOR-TRMR 10K 10\% C TOP-ADJ 1-TURN | 30983 | ET50W103 |
| A1R19 | 0683-1035 | 13 | RESISTOR 10K 5\% .25W FC TC=-400/+700 | 01121 | CB1035 |
| A1R20 | 0683-3025 | 1 | RESISTOR 3K 5\% .25W FC TC=-400/+700 | 01121 | CB3025 |
| A1R21 | 0675-1021 |  | RESISTOR 1K 10\% .125W CC TC=-330/+800 | 01121 | BB1021 |
| A1R23 | 0698-5176 |  | RESISTOR $5105 \% .125 \mathrm{~W}$ CC TC=-330/+800 | 01121 | BB5115 BB3615 |
| A1R24 | 0698-5176 |  | RESISTOR $5105 \% .125 \mathrm{~W}$ CC TC=-330/+800 | 01121 | BB5115 |
| A1R25 | 0698-8356 | 2 | RESISTOR 56K 5\% .125W CC TC=-466/+875 | 01121 | BB5635 |
| A1R26 | 0698-5426 | 4 | RESISTOR 10K 10\% .125W CC TC=-350/+857 | 01121 | BB1031 |
| A1R27 | 0698-5160 |  | RESISTOR 2K 5\% .125W CC TC=-350/+857 | 01121 | BB2025 |
| A1R28* | 0698-5176 | 4 | RESISTOR $5105 \% .125 \mathrm{~W}$ CC TC=-330/+800 | 01121 | BB5115 |
| A1R28* | 0675-1021 | 1 | RESISTOR 1K 10\% . 125 w CC TC $=-330 /+800$ | 01121 | BB1021 |
| A1R28* | 0698-6241 | 1 | RESISTOR $4305 \% .125 \mathrm{~W}$ CC TC= $=-330 /+800$ | 01121 | BB7515 |
| A1R29 | 0683-1035 |  | RESISTOR 10K $5 \% .25 \mathrm{~W}$ FC TC=-400/+700 | 01121 | BB1035 |
| A1R30 | 0698-5175 |  | RESISTOR $3605 \% .125 \mathrm{~W}$ CC TC=-330/+800 | 01121 | BB3615 |
| A1R31 | 1810-0171 | 1 | NETWORK -RES 6-PIN-SIP . 15-PIN-SPCG | 28480 | 1810-0171 |
| A1R32 | 0698-8356 |  | RESISTOR 56K 5\% .125W CC TC=-466/+875 | 01121 | BB5635 |
| A1R33 | 0698-5175 |  | RESISTOR $3605 \% .125 \mathrm{~W}$ CC TC=-330/+800 | 01121 | BB3615 |
| A1R34 | 0698-5175 |  | RESISTOR $3605 \% .125 \mathrm{~W}$ CC TC $=-330 /+800$ | 01121 | BB3615 |
| A1R36 | 0683-5635 | 1 | RESISTOR $56 \mathrm{~K} 5 \% .25 \mathrm{~W}$ FC TC $=-400 /+800$ | 01121 | CB1045 |
| A1R37 | 0683-3915 | 1 | RESISTOR $3905 \% .25 W$ FC TC=-400/+600 | 01121 | CB3915 |
| A1R38 | 0698-5180 |  | RESISTOR 2K 5\% .125W CC TC=-350/+857 | 01121 | BB2025 |
| A1R39 | 0683-1035 |  | RESISTOR 10K 5\% .25W FC TC=-400/+700 | 01121 | CB1035 |
| A1R40 | 0683-1035 |  | RESISTOR 10K 5\% .25W FC TC=-400/+700 | 01121 | CB1035 |
| A1R41 A1R42 | 0683-3035 | 1 | RESISTOR 30K 5\% .25W FC TC=-400/+800 | 01121 | CB3035 |
| A1R43 | 0683-6205 | 1 | RESISTOR 62.25 W FC TC $=-400 /+500$ | 01121 01121 | CB6205 CB1025 |
| A1R44 | 2100-2030 | 1 | RESISTOR -TRMR 20K 10\% C TOP-ADJ 1-TURN | 30983 | ET50W203 |
| A1R45 | 0683-1025 |  | RESISTOR 1K 5\% .25W FC TC=-400/+600 | 01121 | CB1025 |
| A1R46 | 0683-1025 |  | RESISTOR 1K 5\% .25W FC TC=-400/+600 | 01121 | CB1025 |

Table 9E-6-1. Replaceable Parts (Cont'd)

| Reference Designation | HP Part Number | Qty | Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A1R47 | 1810-0041 | 2 | NETWORK-RES 9-PIN-SIP . 15-PIN-SPCG | 28480 | 1810-0041 |
| A1R48 | 0683-5115 | 6 | RESISTOR $5105 \% .25 \mathrm{~W}$ FC TC=-400/+600 | 01121 | C85115 |
| A1R49 | 0683-1025 |  | RESISTOR 1K $5 \% .25 \mathrm{~W}$ FC TC=-400/+600 | 01121 | C81025 |
| A1R50 | 0683-1025 |  | RESISTOR $1 \mathrm{~K} 5 \% .25 \mathrm{~W}$ FC TC=-400/+600 | 01121 | C81025 |
| A1R51 | 0683-1025 |  | RESISTOR 1K 5\% . 25 W FC TC=-400/+600 | 01121 | C81025 |
| A1R52 | 1810-0041 |  | NETWORK-RES 9-PIN-SIP . 15 -PIN-SPCG | 28480 | 1810-0041 |
| A1R53 | 0683-3015 | 1 | RESISTOR 300 5\% . 25 W FC TC=-400/+600 | 01121 | C83015 |
| A1R54 A1R55 | 0683-2015 |  | RESISTOR $2005 \%$. 25 w FC TC=-400/+600 | 01121 | C82015 |
| A1R55 A1R56 | -1810-0139 | 1 | NETWORK-RES 5 -PIN-SIP 15 . 15 -PIN-SPCG RESISTOR $5105 \% .25 \mathrm{WC}$ TC=-400/ 600 | 28480 01121 | $1810-0139$ C 85115 |
| A1R57 | 0683-1035 |  | RESISTOR 10K 5\% . 25 W FC TC=-400/+700 | 01121 | CB1035 |
| A1R58 | 0683-1035 |  | RESISTOR 10K $5 \% .25 \mathrm{~W}$ FC TC=-400/+700 | 01121 | CB1035 |
| A1R59 | 0683-2245 | 1 | RESISTOR $220 \mathrm{~K} 5 \% .25 \mathrm{~W}$ FC TC=-800/+900 | 01121 | CB2245 |
| A1R60 A1R61 | 0683-7525 | 1 | RESISTOR $7.5 \mathrm{~K} 5 \% .25 \mathrm{~W}$ FC TC=-400/+700 RESISTOR $100 \mathrm{~K} 5 \% .25 \mathrm{WFC}$ TC $=-400 /+800$ | 01121 01121 | CB7525 C81045 |
| A1R62 | 0683-3345 | 2 | RESISTOR 330K 5\% . 25 W FC TC=-800/+900 |  |  |
| A1R63 | -0683-6215 | 2 | RESISTOR $6205 \% .25 W \mathrm{FC}$ TC $=-400 /+600$ | 01121 | ${ }^{\text {C86215 }}$ |
| A1R64 | 0683-1035 |  | RESISTOR $10 \mathrm{~K} 5 \% .25 \mathrm{~W}$ FC TC=-400/+700 | 01121 | CB1035 |
| A1R65 | 0683-1025 |  | RESISTOR 1K 5\% . 25W FC TC=-400/+600 | 01121 | CB1025 |
| A1R66 | 0683-2015 |  | RESISTOR $2005 \% .25 \mathrm{~W}$ FC TC=-400/+600 | 01121 | C82015 |
| A1R67 | 0683-1035 |  | RESISTOR 10K 5\% . 25 W FC TC=-400/+700 | 01121 | C81035 |
| A1R68 | 0683-5115 |  | RESISTOR 510 5\% . 25 W FC TC=-400/+600 | 01121 | C85115 |
| A1R69 | 0683-5115 |  | RESISTOR $5105 \% .25 \mathrm{~W}$ FC TC $=-400 /+600$ | 01121 | C85115 |
| A1R71 | 2100-1738 |  | RESISTOR-TRMR 10K 10\% C TOP-ADJ 1-TURN | 30983 | ET50W103 |
| A1R72 | 0683-5115 |  | RESISTOR 510 5\% . 25 W FC TC=-400/+600 | 01121 | C85115 |
| A1R73 A1R74 | 0683-5115 |  | RESISTOR $5105 \%$. 25 W FC TC=-400/+600 | 01121 | C85115 |
| A1R17 A1R75 | -0683-1035 | 1 | RESISTOR $10 \mathrm{~K} 5 \% .25 \mathrm{~W}$ FC TC $=-400 /+700$ | 01121 | CB5105 |
| A1R76 | 0683-5125 | 3 | RESISTOR 5.1K $5 \%$. 25 W FC TC=-400/+700 | 01121 | C85125 |
| A1R77 | 0683-1045 |  | RESISTOR 100K $5 \% .25 \mathrm{~W}$ FC TC $=-400 /+800$ | 01121 | C81045 |
| A1R78 A1R79 | $0683-5125$ $2100-1984$ | 1 | RESISTOR 5.IK $5 \%$. 25 W FC TC=-400/+700 RESISTOR-TRMR $10010 \%$ C TOP-ADJ 1-TURN | 01121 30983 | C85125 ET50WI01 |
| A1R80 |  | 1 | NOT ASSIGNED |  |  |
| A1R81 | 0683-8235 | 1 | RESISTOR 82K 5\% .25W FC TC=-400/+800 | 01121 | C88235 |
| A1R82 | 0683-9135 | 1 | RESISTOR 91K 5\%.25W FC TC=-400/+800 | 01121 | C89135 |
| A1R83 A1R84 | 0683-2715 | 1 | RESISTOR $2705 \% .25 \mathrm{~W}$ FC TC $=-400 /+600$ RESISTOR $330 \mathrm{~K} 5 \% .25 \mathrm{~W}$ FC TC $=800 /+900$ | 01121 01121 | CB2715 C 83345 |
| A1R85 | 0683-1025 |  | RESISTOR $1 \mathrm{~K} 5 \% .25 \mathrm{~W}$ FC TC $=-400 /+600$ | 01121 | CB1025 |
| A1R86 A1R87 | -0683-4305 | 1 | RESISTOR $435 \% .25 \mathrm{~W}$ FC TC= $=-400 /+500$ RESISTOR $10 \mathrm{~K} 5 \% .25 \mathrm{~W}$ FC TC $=-400 /+700$ | 01121 01121 | $\begin{aligned} & \text { CB4305 } \\ & \text { C81035 } \end{aligned}$ |
| A1R88 | 0683-3615 | 1 | RESISTOR 360 5\% .25W FC TC=-400/+600 | 01121 | C83615 |
| A1R89 | 0683-6245 | 1 | RESISTOR 620K $5 \%$. 25 W FC TC $=-800 /+900$ | 01121 | C86245 |
| A1R90 | 0683-6225 | 1 | RESISTOR 6.2K $5 \% .25 \mathrm{w}$ FC TC=-400/+700 | 01121 | C86225 |
| A1R91 | 0683-5125 |  | RESISTOR 5.1K $5 \% .25 \mathrm{~W}$ FC TC=-400/+700 | 01121 | C85125 |
| A1R92 | 0683-1035 |  | RESISTOR 10K $5 \% .25 \mathrm{~W}$ FC TC=-400/+700 | 01121 | C81035 |
| A1R93 | 0683-1035 |  | RESISTOR 10K 5\% .25W FC TC=-400/+700 | 01121 | C81035 |
| A1R94 | 0683-1215 |  | RESISTOR $1205 \% .25 \mathrm{~W}$ | 01121 | C81215 |
| A1S1 A1S2 | $3100-3373$ $3100-3306$ | 1 | SWITCH-RTRY 4P4T-NS . 562 -CTR-SPCG SWITCH-RTRY 3P6T-NS 562 IN CTR SPCG | 28480 28480 | 3100-3373 |
| A1TP1 | 0360-0124 | 10 | TERMINAL-STUD SGL-PIN PRESS-MTG | 28480 | 0360-0124 |
| A1TP2 | 0360-0124 |  | TERMINAL-STUD SGL-PIN PRESS-MTG | 28480 | 0360-0124 |
| A1TP3 | 0360-0124 |  | TERMINAL-STUD SGL-PIN PRESS-MTG | 28480 | 0360-0124 |
| A1TP4 A1TP5 | $0360-0124$ $0360-0124$ |  | TERMINAL-STUD SGL-PIN PRESS-MTG TERMINAL-STUD SGL-PIN PRESS-MTG | 28480 28480 | $0360-0124$ $0360-0124$ |
| A1TP6 | 0360-0124 |  | TERMINAL-STUD SGL-PIN PRESS-MTG | 28480 | 0360-0124 |
| A1TP7 | 0360-0124 |  | TERMINAL-STUD SGL-PIN PRESS-MTG | 28480 | 0360-0124 |
| A1TP8 | 0360-0124 |  | TERMINAL-STUD SGL-PIN PRESS-MTG | 28480 | 0360-0124 |
| A1TP9 | 0360-0124 |  | TERMINAL-STUD SGL-PIN PRESS-MTG | 28480 | 0360-0124 |
| A1TP10 | 0360-0124 |  | TERMINAL-STUD SGL-PIN PRESS-MTG | 28480 | 0360-0124 |
| A1U1 | 1826-0275 | 1 | IC 78L12AC V RGL TR | 04713 | MC78L12CP |
| A1U2 | 1826-0274 | 1 | IC 78L15AC V RGL TR | 04713 | MC78L15CP |
| A1U3 | $1820-0621$ $1820-0910$ | 1 |  | 01295 01295 | SN7438N SN74LS83N |
| A1U5 | 1820-1166 | 1 | IC-DIGITAL DM85L51N TTL QUAD | 27014 | DM85L51N |
| A1U6 | 1820-1224 | 1 | IC-DIGITAL ECL TPL MC10216P 2 LINE RCVR | 04713 | MC10216P |
| A1U7 A1U8 | $1820-0493$ $1826-0139$ | 2 1 | IC LM 307 OP AMP IC MC 1458 OP AMP | 27014 04713 | LM307N |
| A1U9 | 1820-0681 | 1 | IC-DIGITAL SN74SOON TTL SQUAD 2 NAND | 01295 | SN74S00N |
| A1U10 | 1820-1206 | 1 | IC-DIGITAL SN74LS27N TTL LS TPL 3 NOR | 01295 | SN74LS27N |
| A1U11 | 1820-1443 | 1 | IC-DIG ITAL SN74LS293N TTL LS BIN | 01295 | SN74LS293N |
| A1U12 | 1820-0804 | 1 | IC-DIGITAL MC10106P ECL TPL NOR | 04713 | MC10106P |
| A1U13 A1U14 | $1820-0803$ $1820-1383$ | 1 | $\begin{array}{lr}\text { IC-DIGITAL MC10105P ECL } \\ \text { IC-DIGITAL MC10138L ECL } & \text { OR-NOR } \\ \text { ICL }\end{array}$ | 04713 04713 | MC10105P |
| A1U15 | 1826-0174 | 2 | IC-DIGITAL MC10138L ECL IC MC | $\begin{aligned} & 04713 \\ & 28480 \end{aligned}$ | $\begin{aligned} & \text { MC10138L } \\ & 1826-0174 \end{aligned}$ |

Table 9E-6-1. Replaceable Parts (Cont'd)

| Reference Designation | HP Part Number | Qty | Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A1U16 | 1826-0174 |  | IC MC 3302 COMPARATOR | 28480 | 1826-0174 |
| A1U17 | 1820-0817 | 1 | IC-DIGITAL MC10131P ECL DUAL D-M/S | 04713 | MC10131P |
| A1U18 | 1820-0584 | 1 | IC-DIGITAL DM74L02N TTL L QUAD 2 NOR | 27014 | DM74L02N |
| A1U19 | 1820-1442 | 3 | IC-DIGITAL SN74LS290N TTL LS | 01295 | SN74LS290N |
| A1U20 | 1820-1442 |  | IC-DIGITAL SN74LS290N TTL LS DECD | 01295 | SN74LS290N |
| A1U21 | 1820-1442 |  | IC-DIGITAL SN74LS290N TTL LS DECD | 01295 | SN74LS290N |
| A1U22 | 1820-0630 | 1 | IC-DIGITAL MC4044P TTL | 04713 | MC4O44P |
| A1U23 | 1820-0493 |  | IC LM 307 OP AMP | 27014 | LM307N |
| A1U24 A1U25 | $1820-1307$ $1820-1225$ | 1 | IC-DIGITAL SN74S132N TTL S QUAD 2 NAND IC-DIGITAL MC10231P ECL DUAL | 01295 04713 | SN74S132N MC10231P |
| A1U25 | 1820-1225 | 1 | IC-DIGITAL MC10231P ECL DUAL D-M/S | 04713 | MC10231P |
| A1U26 | 1820-0712 | 1 | IC:BINARY | 28480 | 1820-0712 |
| A1U27 | 1826-0147 | 1 | IC 7812C V RGLTR | 07263 | 7812 UC |
| A1U28 | 1826-0122 | 1 | IC 7805C V RGLTR | 07263 | 7805UC |
|  |  |  | A1 MISCELLANEOUS |  |  |
|  | 0380-0342 | 4 | STANDOFF-RVT-ON .125LG 6-32THD . 250 D BRS | 28480 | 0380-0342 |
|  | 0905-0479 $05354-00009$ | 1 5 | GASKET <br> GROUND SPRING, GOLD | 28480 28480 | $\begin{aligned} & 0905-0479 \\ & 05354-00009 \end{aligned}$ |
| A2 | 05305-60006 | 1 | 1.3 GHZ BOARD ASSEMBLY | 28480 | 05305-60006 |
| A2C1 | 0160-3878 | 10 | CAPACITOR-FXD 1000PF +-20\% 100WVDC CER | 28480 | 0160-3878 |
| ${ }^{\text {A2C2 }} 2$ | 0160-3878 |  | CAPACITOR-FXD 1000PF + - $20 \%$ 100WVDC CER | 28480 | 0160-3878 |
| A2C3 | 0160-3878 |  | CAPACITOR-FXD 1000PF +-20\% 100WVDC CER | 28480 | 0160-3878 |
| A2C4 | $0160-0570$ $0160-3878$ | 3 | CAPACITOR-FXD 220PF +-20\% 100WVDC CER CAPACITOR-FXD 1000PF +-20\% 100WVDC CER | 28480 28480 | $\begin{aligned} & 0160-0570 \\ & 0160-3878 \end{aligned}$ |
| A2C6 | 0160-0570 |  | CAPACITOR-FXD 220PF +-20\% 100WVDC CER | 28480 | 0160-0570 |
| A2C7 | 0160-3878 |  | CAPACITOR-FXD 1000PF +-20\% 100WVDC CER | 28480 | 0160-3878 |
| A2C8 | 0160-0570 |  | CAPACITOR-FXD 220PF +-20\% 100WVDC CER | 28480 | 0160-0570 |
| A2C9 | 0160-3878 |  | CAPACITOR-FXD 1000PF +-20\% 100WVDC CER | 28480 | 0160-3878 |
| A2C10 | 0160-3876 | 1 | CAPACITOR-FXD 47PF +-20\% 200WVDC CER | 28480 | 0160-38710 |
| ${ }^{\text {A } 2 \mathrm{Cl1}}$ | 0160-2599 | 1 | CAPACITOR-FXD 680PF +-10\% 200WVDC CER | 28480 | 0160-2599 |
| $\mathrm{A}^{2} \mathrm{C} 12$ | 0160-3878 |  | CAPACITOR-FXD 1000PF +-20\% 100WVDC CER | 28480 | 0160-3878 |
| ${ }^{\text {A2C13 }}$ | 0160-3878 |  | CAPACITDR-FXD 1000PF +-20\% 100WVDC CER | 28480 | 0160-3878 |
| A2C14 | 0160-3873 $0160-3878$ |  | CAPACITOR-FXD 4.7PF +-.5PF 200WVDC CER CAPACITOR-FXD 1000PF + -20\% 100WVDC CER | 28480 28480 | $0160-3873$ $0160-3878$ |
| A2C16 | 0160-3878 |  | CAPACITOR-FXD 1000PF +-20\% 100WVDC CER | 28480 | 0160-3878 |
| A2C17 | 0160-4415 | 1 | CAPACITOR-FXD . $16 \mathrm{UF}+5-0 \%$ 200WVDC POLYE | 28480 | 0160-4415 |
| A2C18 | 0160-4248 | 1 | CAPACITOR-FXD 1000PF + -10\% 50WVDC CER | 26654 | 38X050S102K (D) |
| A2CR1 | 1902-0032 | 2 | DIODE-ZNR 5.49V $5 \%$ DO-7 PD=.4W TC=+.009\% | 04713 | SZ 10939-107 |
| A2CR2 ${ }_{\text {A2CR3 }}$ | 1901-0050 | ${ }_{1}^{2}$ | DIODE-SWITCHING 80V 200MA 2NS DO-7 | 28480 | 1901-0050 |
| A2CR4 | 1902-3171 $1901-0050$ |  | DIODE-ZNR 11 L 5\% DO-7 PD=.4W TC=+.062\% DIODE-SWITCHING 80V 200MA 2NS DO-7 | 28480 | 1901-0050 |
| A2CR5 | 1901-0639 | 4 | DIODE-PIN 110V | 28480 | 1901-0639 |
| A2CR6 | 1901-0639 |  | DIODE-PIN 110V | 28480 | 1901-0639 |
| A2CR7 | 1902-0032 |  | DIODE-ZNR 5.49V 5\% DO-7 PD=.4W TC=+.009\% | 04713 | SZ 10939-107 |
| A2CR8 | 1901-0639 |  | DIODE-PIN 110 V | 28480 | 1901-0639 |
| A2CR9 A2CR10 | $1901-0639$ $1901-0179$ |  | UIODE-PIN 11OV DIODE-SWITCHING 15V 50MA 750PS DO-7 | 28480 28480 | 1901-0639 $1901-0179$ |
|  |  |  |  |  |  |
| A2CR12 | 1901-0179 |  | DIOOE-SWITCHING 15 V 50MA 750PS DO-7 DIODE-SWITCHING 15V 50MA 750PS DO-7 | 284880 | $1901-0179$ $1901-0179$ |
| A2CR13 | 1901-0040 |  | DIODE-SWITCHING 30V 50MA 2NS DO-35 | 28480 | 1901-0040 |
| A211 | 9140-0144 | 2 | COIL-FXD MOLDED RF CHOKE 4.7UH 10\% | 24226 | 10/471 |
| A2L2 | 9100-2265 | 1 | COIL-MLD 10UH 10\% Q=60.095DX.25LG | 24226 | 10/102 |
| A2L3 | 9140-0144 |  | COIL-FXD MOLDED RF CHOKE 4.7UH 10\% | 24226 | 10/471 |
| A2P1 | 1251-4249 | 10 | CONTACT-CONN U/W-POST-TYPE DPSLDR | 28480 | 1251-4249 |
| A2R1 | 0698-6648 | 4 | RESISTOR $6205 \% .125 \mathrm{WCC}$ TC $=-330 /+800$ | 01121 | B86215 |
| A2R2 | 0698-5426 |  | RESISTOR 10K 10\% .125W CC TC=-350/+857 | 01121 | B81031 |
| A2R3 | $0698-6648$ $0698-7102$ | 1 | RESISTOR $620.5 \% .125 \mathrm{WCC}$ CC= $-330 /+800$ RESISTOR $5.1 \mathrm{~K} 5 \% .125 \mathrm{~W}$ CC TC $=-350 /+857$ | 01121 01121 | B86215 BB5125 |
| A2R5 | 0698-5426 |  | RESISTOR 10K 10\% . 125 W CC TC=-350/+857 | 01121 | BB1031 |
| A2R6 | 0698-6648 |  | RESISTOR $6205 \% .125 \mathrm{~W}$ CC TC=-330/+800 | 01121 | B86215 |
| A2R7 | 0698-6648 |  | RESISTOR $6205 \%$.125W CC TC=-330/+800 | 01121 | B86215 |
| A2R88 A2R9 | 0683-2265 | 1 | RESISTOR 22M 5\% .25W FC TC=-900/+1200 | 01121 | CB2265 |
| A2R9 A2R10 | $0698-3378$ $0698-6984$ | 2 1 | RESISTOR $515 \% .125 \mathrm{~W}$ CC TC=-270/+540 | 01121 01121 | BB5105 BB4715 |
| A2R11 | 0698-7243 | 1 | RESISTOR 1.96K 1\% .05W F TC=0+-100 | 24546 | C3-1/8-T0-1961-G |
| A2R12 | 0698-5426 |  | RESISTOR 10K 10\% .125W CC TC=-350/+857 | 01121 | BB1031 |
| ${ }_{\text {A2R13 }}$ | 2100-1738 |  | RESISTOR-TRMR 10K 10\% C TOP-ADJ 1-TURN | 30983 | ${ }_{\text {ET50W }} \mathrm{C}$-1/8-T0-1473-G |
| A2R24 A2R15 | 0698-7288 | 4 | RESISTOR $147 \mathrm{~K} 1 \%$. 05 WW F TC=0+-100 RESISTOR $147 \mathrm{~K} 1 \% .05 \mathrm{~F}$ TC=0+-100 | 24546 24546 | C3-1/8-T0-1473-G |
| A2R16 | 0698-7288 |  | RESISTOR 147K $1 \%$. 05 WW F TC=0+-100 | 24546 | C3-1/8-T0-1473-G |
| A2R17 | 0698-7288 |  | RESISTOR 147K 1\% .05W F TC=0+-100 | 24546 | C3-1/8-T0-1473-G |
| A2R18 A2R19 | 0698-5174 | 1 | RESISTOR $2005 \% .125 \mathrm{~W}$ CC TC $=-3301+800$ | 01121 | B82015 |
| A2R20 | 0683-9105 | 1 | RESISTOR $915 \% .25$ W FC TC= $=400 /+500$ | $01121$ | BB8215 CB9105 |

Model 5305B
Replaceable Parts
Table 9E-6-1. Replaceable Parts (Cont'd)

| Reference Designatlon | HP Part Number | Qty | Description | Mfr Code | Mfr Part Number |
| :---: | :---: | :---: | :---: | :---: | :---: |
| A2R21 A2R22 A2R23 A2R24 A2R25 A2U1 A2U2 A2U3 A2U4 | 0698-4132 <br> 2100-1986 <br> 0698-3442 <br> 0698-3378 <br> 0811-3468 <br> 1820-0223 <br> 5088-7017 <br> $1820-1695$ $1820-1694$ | $\begin{aligned} & 1 \\ & 1 \\ & 1 \\ & \\ & 1 \\ & 1 \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ | RESISTOR $625 \%$. 125 W CC TC $=-270 /+540$ RESISTOR-TRMR 1 K 10\% C TOP-ADJ 1 -TURN RESISTOR 237 1\% .125W F TC-0 +-100 RESISTOR $515 \%$.125W CC TC $=-270 /+540$ RESISTOR 240 1\% IC LM 301A OP AMP 1.3 GHZ AMPLIFIER IC-DIGITAL <br> IC-DIGITAL | 01121 30983 16299 01121 07088 27014 28480 28480 28480 | 886205 ET50W102 <br> C4-1/8-TO-237R-F BB5105 KP50 LM301AH 5088-7017 1820-1695 1820-1694 |
| A2W1 | $\begin{aligned} & 05305-60207 \\ & 1250-0821 \\ & 1250-0857 \\ & 05255-2010 \end{aligned}$ | $\begin{aligned} & 1 \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ | 1.3 GHZ CABLE <br> CONNECTOR-RF SMC FEM UNMTD FERRULE CLAMP:RF CONNECTOR SLEEVE, COAX <br> A2 MISCELLANEOUS | $\begin{aligned} & 28480 \\ & 24931 \\ & 77068 \\ & 28480 \end{aligned}$ | $\begin{aligned} & \text { 05305-60207 } \\ & 37 P 102-1 \\ & 30994-4 \\ & 05255-2010 \end{aligned}$ |
|  | 05305-00010 05305-20107 | $\begin{aligned} & 3 \\ & 1 \end{aligned}$ | CLAMP, GRINDING <br> HOUSING, AMPLIFIER <br> CHASSIS PARTS | $\begin{aligned} & 28480 \\ & 28480 \end{aligned}$ | $\begin{aligned} & 05305-00010 \\ & 05305-20107 \end{aligned}$ |
| F1 | 2110-0301 | 2 | FUSE .125A 125V FAST-bLO . 281 X .093 | 75915 | 275,125 |
| J1 | 1250-0186 05305-20104 05305-60205 05305-60206 5060-0467 | $\begin{aligned} & 1 \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ | CONNECTOR-RF BNC FEM SGL HOLE FR HOLDER, FUSE <br> CONNECTOR ASSEMBLY, BNC CONNECTOR ASSEMBLY, SMC MALE PROBE, POWER | 90949 28480 28480 28480 28480 | 31-221-1024 <br> 05305-20104 <br> 05305-60205 <br> 05305-60206 <br> 5060-0467 |
| J2 |  |  | MISCELLANEOUS PARTS |  |  |
|  | $\begin{aligned} & 0370-1099 \\ & 0510-0076 \\ & 0590-0038 \\ & 1460-1311 \\ & 1460-1312 \end{aligned}$ | $\begin{aligned} & 3 \\ & 2 \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ | KNOB-BASE-PTR. 5 IN JGK SGI-DECAL NUT-SHMET 6-32-THD .63-WD STL NUT-HEX-DBL-CHAM 1/2-32-THD .094-THK SPRING-LEAF .25-W .58-LG BE CU SPRING-EXT.25-W .58-LG BE CU | $\begin{aligned} & 28480 \\ & 78553 \\ & 28480 \\ & 28480 \\ & 28480 \end{aligned}$ | $\begin{aligned} & 0370-1099 \\ & \text { C8599-632-24B } \\ & 0590-0038 \\ & 1460-1311 \\ & 1460-1312 \end{aligned}$ |
|  | $\begin{aligned} & 2950-0043 \\ & 9220-1762 \\ & 5040-7032 \\ & 05300-00006 \\ & 05300-20010 \end{aligned}$ | $\begin{aligned} & 5 \\ & 1 \\ & 1 \\ & 2 \\ & 1 \end{aligned}$ | NUT-HEX-DBL-CHAM 3/8-32-THD .094-THK <br> DUST COVER <br> FOOT, REAR <br> CLIP, RFI <br> CASE | $\begin{aligned} & 73743 \\ & 28880 \\ & 28480 \\ & 28880 \\ & 28480 \end{aligned}$ | $\begin{aligned} & 2 \times 28200 \\ & 9220-1762 \\ & 5040-7032 \\ & 05300-00006 \\ & 05300-20010 \end{aligned}$ |
|  | 05300-40003 05300-40004 05301-20005 05301-40001 $05305-00005$ 05305-00005 | $\begin{aligned} & 4 \\ & 4 \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ | SUPPORT, BOARD <br> GUIDE, SLIDE <br> STAND, TILT FOOT <br> PANEL, FRONT | $\begin{aligned} & 28480 \\ & 28480 \\ & 28480 \\ & 28480 \\ & 28480 \end{aligned}$ | $\begin{aligned} & 05300-40003 \\ & 05300-40004 \\ & 05301-20005 \\ & 05301-40001 \\ & 05305-00005 \end{aligned}$ |
|  | 05305-00006 05305-00007 05305-00008 05305-00009 05305-00011 | $\begin{aligned} & 1 \\ & 1 \\ & 1 \\ & 1 \\ & 1 \\ & 1 \end{aligned}$ | PANEL, REAR BRACKET, FRONT COVER, HOUSING SHIELD, AMPLIFIER BRACKET, 80 MHZ | $\begin{aligned} & 28480 \\ & 28480 \\ & 28480 \\ & 28480 \\ & 28480 \end{aligned}$ | 05305-00006 05305-00007 05305-00008 05305-00009 05305-00011 |
|  | $\begin{aligned} & \text { 05305-20105 } \\ & 05354-00009 \end{aligned}$ | 1 | insulator GROUND SPRING, GOLD | $\begin{aligned} & 28480 \\ & 28480 \end{aligned}$ | $\begin{aligned} & \text { 05305-20105 } \\ & 05354-00009 \end{aligned}$ |

Figure 6-1. Details of Input Connector J1 and Fuse Mounting


Table 9E-6-2. Manufacturers Code List

| Mfr No. | Manufacturer Name | Address | Zip Code |
| :---: | :--- | :--- | :---: |
| 01121 | Allen-Bradley Co | Milwaukee, WI | 53212 |
| 01295 | Texas Instr Inc Semicond Cmpnt Div | Dallas, TX | 75231 |
| 04713 | Motorola Semiconductor Products | Phoenix, AZ | 85008 |
| 07263 | Fairchild Semiconductor Div | Mountain View, CA | 94040 |
| 09023 | Cornell-Dubilier Elek Div Fed Pac | Sanford, NC | 27330 |
| 16299 | Corning Gl Wk Elec Cmpnt Div | Raleigh, NC | 27604 |
| 24226 | Gowanda Electronics Corp | Gowanda, NY | 14070 |
| 24546 | Corning Glass Works (Bradford) | Bradford, PA | 16701 |
| 24931 | Speciality Connector Co Inc | Indianapolis, IN | 46227 |
| 26654 | Varadyne Inc | Santa Monica, CA | 90403 |
| 27014 | National Semiconductor Corp | Santa Clara, CA | 95051 |
| 28480 | Hewlett-Packard Co Corporate HQ | Palo Alto, CA | 94304 |
| 30983 | Mepco/Electra Corp | San Diego, CA | 92121 |
| 56289 | Sprague Electric Co | North Adams, MA | 01247 |
| 71785 | TRW Elek Components Cinch Div | Elk Grove Village, IL | 60007 |
| 72136 | Electro Motive Corp Sub IEC | Willimantic, CT | 06226 |
| 73743 | Fischer Special Mfg Co | Cincinnati, OH | 45206 |
| 75915 | Littlefuse Inc | Des Plaines, IL | 60016 |
| 77068 | Bendix Corp Electrodynamics Div | North Hollywood, CA | 91605 |
| 78533 | Tinnerman Products Inc | Cleveland, OH | 44129 |
| $9 D 949$ | Amphenol Sales Div of Bunker-Ramo | Hazelwood, MO | 63042 |

## SECTION IX E

## 5305B 1300 MHz COUNTER

## SUBSECTION VII

MANUAL CHANGES

## 9E-7-1. MANUAL CHANGES

9E-7-2. Section IX E applies directly to model 5305B 1300 MHz Counters having Serial Prefix number 1616A.

## 9E-7-3. NEWER INSTRUMENTS

9E-7-4. As changes are made, newer instruments may have serial number prefixes not listed in Section IX E. The manuals for these instruments will be sup plied with "Manual Changes" sheets containing the required information; replace the affected pages with the replacement "manual changes" pages. Contact the nearest Hewlett-Packard Sales and Service Office for information if these pages are missing.

## SECTION IX E <br> 5305B 1100 MHz COUNTER <br> SUBSECTION VIII <br> CIRCUIT DIAGRAMS

## 9E-8-1. INTRODUCTION

9E-8-2. This subsection of the manual contains the following information:
a. A signal list that gives the signal name and connector pin number of each signal that interconnects with the mainframe (see Table 9E-8-1).
c. Component location views of the printed-circuit boards.
d. Schematic diagrams of the counter.

9E-8-3. Use the information in this subsection in conjunction with the information provided in Subsection V, Maintenance, while troubleshooting the counter.
b. Signal waveforms at key points in the input amplifiers and clock circuits.

Table 9E-8-1. Counter Signal List

| $\begin{aligned} & \text { PIN } \\ & \text { NO. } \end{aligned}$ | SIGNAL NAME | DESCRIPTION |
| :---: | :---: | :---: |
| 1 2 3 | $\left.\begin{array}{l} +5 \mathrm{~V} \\ -5 \mathrm{~V} \\ -17 \mathrm{~V} \end{array}\right\}$ | Circuit operating voltages. |
| 4 | COMMON RETURN | Common power and signal return line. |
| 5 | $\begin{gathered} \text { F1 } \\ \text { "9" } \end{gathered}$ | Signal to be countend in the mainframe. <br> Goes low when the mainframe counter reaches $9 \%$ full-scale. Tied to pin 14. |
| 7 | F2 | Time Base Clock 10 MHz or $10 \mathrm{MHz} \div 16$. |
| 8 | INHIBIT | High during the measurement cycle, low during the display cycle. |
| 9 | OPEN | Low signal forces the main gate flip-flop in the mainframe to the open position. |
| 10 | CLOSE | Low signal forces the main gate flip-flop in the mainframe to the closed position. |
| $\begin{aligned} & 11 \\ & 12 \end{aligned}$ | LOG NC | Logarithmic pulse tram from time base triggers main gate flipflop on rising edge. |
| 13 | EXPONENT | Inverted $\log$ pulses while main gate in mainframe is open indicates number of auto-ranging steps. |
| 14 | OVERFLOW | Low signal enables overflow and storage. |
| 15 | RESET | High signal resets all registers. |
| 16 | CLOCK | 10 MHz reference signal from mainframe crystal oscillator. |
| 17 | MAX TIME | Low signal enables closing of the main gate in the mainframe on next $\log$ pulse. Rising edge initiates display cycle. |
| 18 | TIME BASE OUTPUT | Output from the time base decade that is selected by the time base select code on pins 22, 23, and 24. |

Table 9E-8-1. Counter Signal List (Continued)

| PIN NO. | SIGNAL NAME | DESCRIPTION |
| :---: | :---: | :---: |
| 19 | PRINT | Low signal provides print command to rear panel connector on |
| 20 | TRANSFER | Low signal transfers data to display. High signal stores data. |
| 21 | 1 MHz TIME BASE INPUT |  |
| $\begin{aligned} & 22 \\ & 23 \\ & 24 \end{aligned}$ | $\left.\begin{array}{l}\text { TIME BASE SELECT A } \\ \text { TIME BASE SELECT B } \\ \text { TIME BASE SELECT C }\end{array}\right\}$ | Time base select code $\mathrm{A}, \mathrm{B}$, and C determines the time base frequency at the time base output, pin 18. |
| 25 | +22 V | Full wave rectified voltage from the power transformer secondary. Provides power to charge the battery pack. If no battery pack is used, pin 25 is connected via the plug-on to pin 50 (DC-IN). |
| 26 | $+17 \mathrm{~V}$ |  |
| 27 | Hz |  |
| 28 | $M \quad$ | Pins 27 through 31 provide the drive to the annunciator lights on the front panel. A low signal lights the corresponding |
| 29 | S | indicator. |
| 30 | K |  |
| 31 | $\mathrm{u} \quad)$ | Low signal from front panel pushbutton switch or rear panel input |
| 32 | MAN RES | clears the system to zero. |
| 33 | DP1 | Low signal activates decimal point 1. |
| 34 | DP2 | Low signal activates decimal point 2. |
| 35 | NC |  |
| $\begin{aligned} & 36 \\ & 37 \end{aligned}$ | DIGIT ADDRESS X DIGIT SELECT X X | Digit address code $\mathrm{X}, \mathrm{Y}, \mathrm{Z}$ from the display scanner indicates which data digit is being displayed. |
| $\begin{aligned} & 38 \\ & 39 \\ & 40 \\ & 41 \end{aligned}$ | $\left.\begin{array}{l}\text { DIGIT ADDRESS Y } \\ \text { DIGIT SELECT Y } \\ \text { DIGIT ADDRESS Z } \\ \text { DIGIT SELECT Z }\end{array}\right\}$ | Digit select code $\mathrm{X}, \mathrm{Y}, \mathrm{Z}$ is the code that selects the mainframe counter digit that is to be displayed. If the mainframe counter is displayed directly, the corresponding lines of the digit address code and the digit select code are connected together. |
| $\begin{aligned} & 42 \\ & 43 \\ & 44 \\ & 45 \end{aligned}$ | $\left.\begin{array}{l}\text { DATA "D" } \\ \text { DATA "C"' } \\ \text { DATA "B" } \\ \text { DATA "A" }\end{array}\right\}$ | The data code A, B, C, D represents the digit to be displayed in binary coded decimal form. Data lines can carry the mainframe counter information to the plug-on as well as to the display or can bypass the counter and bring plug-on data to the display. |
| 46 | DP3 | Low signal activates decimal point 3. |
| 47 | DP4 | Low signal activates decimal point 4. |
| 48 | DP5 | Low signal activates decimal point 5. |
| 49 | COMMON RETURN | Common power and signal return line. |
| 50 | DC IN | DC Power to power supply from battery pack or from 22 volt input power at pin 25. |

Input Signal: 9 kHz at 1 V rms
Oscilloscope: HP 180A/1801A/1821A with 10:1 probe
Oscilloscope Settings: DISPLAY: ALT
(Unless otherwise stated) POLARITY: +
Coupling: A.C.
TIME/DIV: . 1 ms


1. .1V /DIV
2. . $01 \mathrm{~V} / \mathrm{DIV}$

(5)

3. .05V/DIV
4. . $2 \mathrm{~V} / \mathrm{DIV}$
5. . $02 \mathrm{~V} / \mathrm{DIV}$
6. . $05 \mathrm{~V} / \mathrm{DIV}$

Part of Figure 8-1. Channel B and Frequency Multiplier Circuits, Schematic Diagram (Cont'd)


9E-8-4

## NOTES

1. REFERENCE DESIGNATIONS WITHIN THIS ASSEMBLY ARE ABBREVIATED. ADD ASSEMBLY NUMBER TO ABBREVIATION FOR COMPLETE DESCRIPTION.
2. UNLESS OTHERWISE INDICATED:

RESISTANCE IN OHMS;
CAPACITANCE IN PICOFARADS
INDUCTANCE IN MICROHENRIES
3. + 17VB -5VA AND + 5VB IS SWITCHED FROM A1S1

A1 TABLE OF ACTIVE ELEMENTS

| Ref. Desig. | HP Part No. | Mfr or Industry Part No. |
| :---: | :---: | :---: |
| CR1, 10, 16, 20, 22 | 1910-0016 |  |
| CR2, 4, 5, 7 | 1901-0179 |  |
| CR3 | 1902-0025 | SZ 10939-182 |
| CR6, 8, 15, 16, 17, | 1901-0040 |  |
| 18, 19 | 1901-0040 |  |
| CR9, 11, 12, 13, 21 | 1901-0535 |  |
| CR14, 23 | 1901-0028 | SR 1358-9 |
| CR24 | 1901-0460 |  |
| Q1 | 1855-0081 | 2N5245 |
| Q2 | 1853-0247 |  |
| Q3 | 1855-0386 | 2N4392 |
| Q4, 7, 14, 17, 19 | 1854-0071 |  |
| 05, 8, 9, 10 | 1853-0015 |  |
| 06 | 1854-0634 | MPS-U01 |
| Q11, 12, 13, 18 | 1854-0009 |  |
| Q15, Q16 | 1853-0036 | SPS-3612 |
| U1 | 1826-0275 | MC78L12CP |
| U2 | 1826-0274 | MC78L15CP |
| U3 | 1820-0621 | SN7438N |
| U4 | 1820-0910 | SN74LS83N |
| U5 | 1820-1166 | DM85L51N |
| U6 | 1820-1224 | MC10216P |
| U7, 23 | 1820-0493 | LM307N |
| U8 | 1826-0139 | MC1458P1 |
| U9 | 1820-0681 | SN74S00N |
| U10 | 1820-1206 | SN74LS27N |
| U11 | 1820-1443 | SN74LS293N |
| U12 | 1820-0804 | MC10106P |
| U13 | 1820-0803 | MC10105P |
| U14 | 1820-1383 | MC10138L |
| U15, 16 | 1826-0174 |  |
| U17 | 1820-0817 | MC10131P |
| U18 | 1820-0584 | DM74LO2N |
| U19, 20, 21 | 1820-1442 | SN74LS290N |
| U22 | 1820-0630 | MC4044P |
| U24 | 1820-1307 | SN74S132N |
| U25 | 1820-1225 | MC10231P |
| U26 | 1820-0712 |  |
| U27 | 1820-0147 | 7812UC |
| U28 | 1826-0122 | 7805UC |



Note: Waveforms 1 thru 10 taken with 1 MHz input and RESOLUTION switch set to .1 Hz . Waveforms 11 thru 16 taken with 100 MHz input.

.05V/DIV, $5 \mu \mathrm{~s} / \mathrm{DIV}, \mathrm{AC} / \mathrm{ALT}$

$.2 \mathrm{~V} / \mathrm{DIV}, .5 \mathrm{~ms} /$ DIV, DC/ALT

.1V/DIV, $5 \mu \mathrm{~s} / \mathrm{DIV}, \mathrm{AC} / \mathrm{ALT}$

.05V/DIV, $.1 \mu \mathrm{~s} /$ DIV, DC/ALT MAG-X10


## A2 1.3 GHz AMPLIFIER

 DC VOLTAGE MEASUREMENTS WITH NO INPUT SIGNAL
## ATTN: MIN

1. CR5 Anode
2. CR5 Cathode
3. CR6 Anode
4. CR6 Anode
5. CR8 Cathode
6. CR9 Anode
7. CR10 Cathode
8. CR12 Cathode
9. CR11 Cathode

8V

RANGE: 1300 MHz ATTN: MAX

1. CR5 Anode 3.1 V
2. CR5 Cathode 2.5 V
3. CR6 Anode 0 V
4. CR6 Cathode 0V
5. CR8 Cathode 1.4 V
6. CR9 Anode 2.0 V
7. CR10 Cathode .82 V
8. CR12 Cathode 8.0 V
9. CR11 Cathode 8.0 V

NOTE
These voltages will vary depending on the position of R13.




